NOTES:
1. All LVDS_IN, CMOS_IN, KEY_IN lines are driven by the Mezzanine Card.
2. All LVDS_OUT, CMOS_OUT lines are driven by the Motherboard.
3. All unused single ended lines must be terminated with 10 KOhm to GND and a 50 Ohm series with 50 pF to GND on the Mezzanine Card.
4. All unused LVDS_OUT lines must be terminated differentially with 100 Ohm on the Mezzanine Card.
5. The 3.3V Power is referenced to Digital Ground "ground."
6. The +2.5V, -2.5V powers can be referenced to the Analog Ground "AGND" or to the Digital Ground.
7. For 20-BIT ADC with Single Ended Interface, use Connection List below.

20-BIT ADC with Single Ended Interface:
- DOU[3:0] -> CMOS_IN[3:0]
- CNVST[3:0] -> CMOS_OUT[3:0]