

1

2

3

4

A

A

B

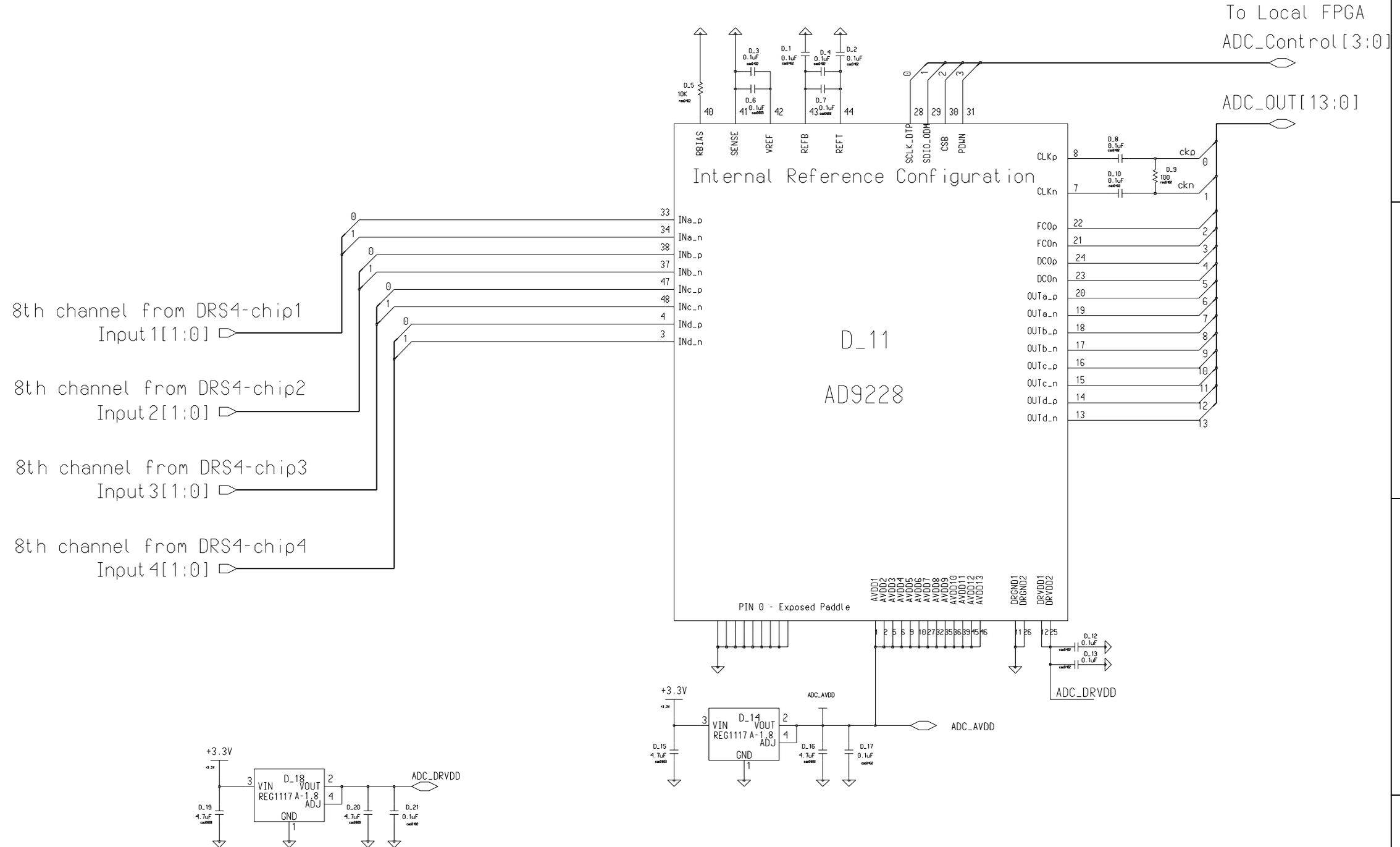
B

C

C

D

D



Engineer	M. Bogdan	The University of Chicago 5640 S. Ellis Ave. Chicago, IL 60637		
Drawn by	M. Bogdan			
R&D CHK				
DATE:	7/11/2011	<b>Aux ADC Block</b>		
TIME:	2:00 pm			
QA CHK		REV A	DRW. 2748	Sheet 5

1

2

3

4