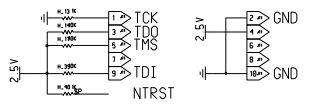
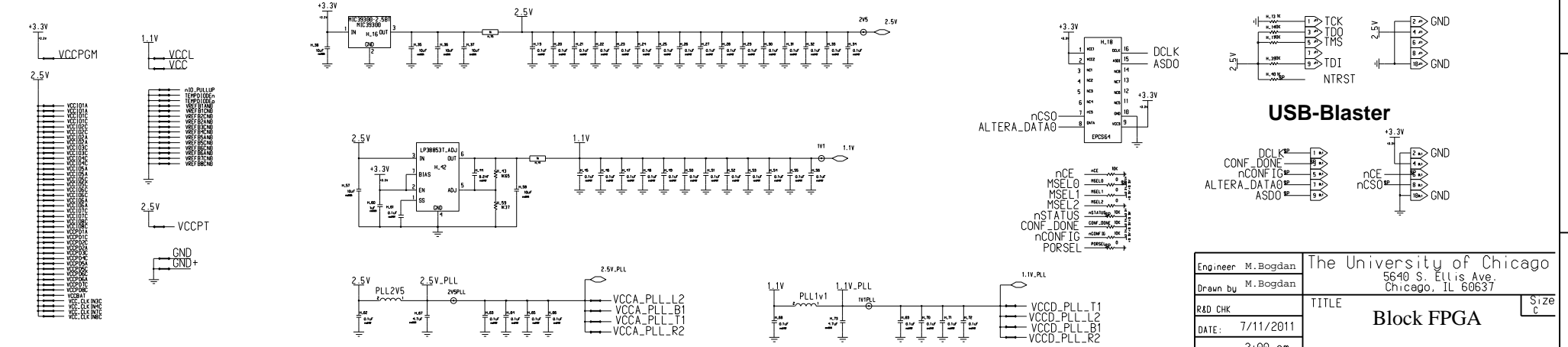
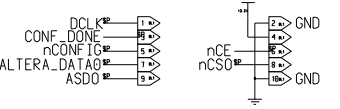


JTAG Connector



USB-Blaster



Engineer	M. Bogdan	The University of Chicago	
Drawn by	M. Bogdan	5640 S. Ellis Ave. Chicago, IL 60637	
R&D CHK		TITLE	Block FPGA
DATE	7/11/2011		
TIME	2:00 pm		
QA CHK		REV	DRW. 2748 Sheet 7