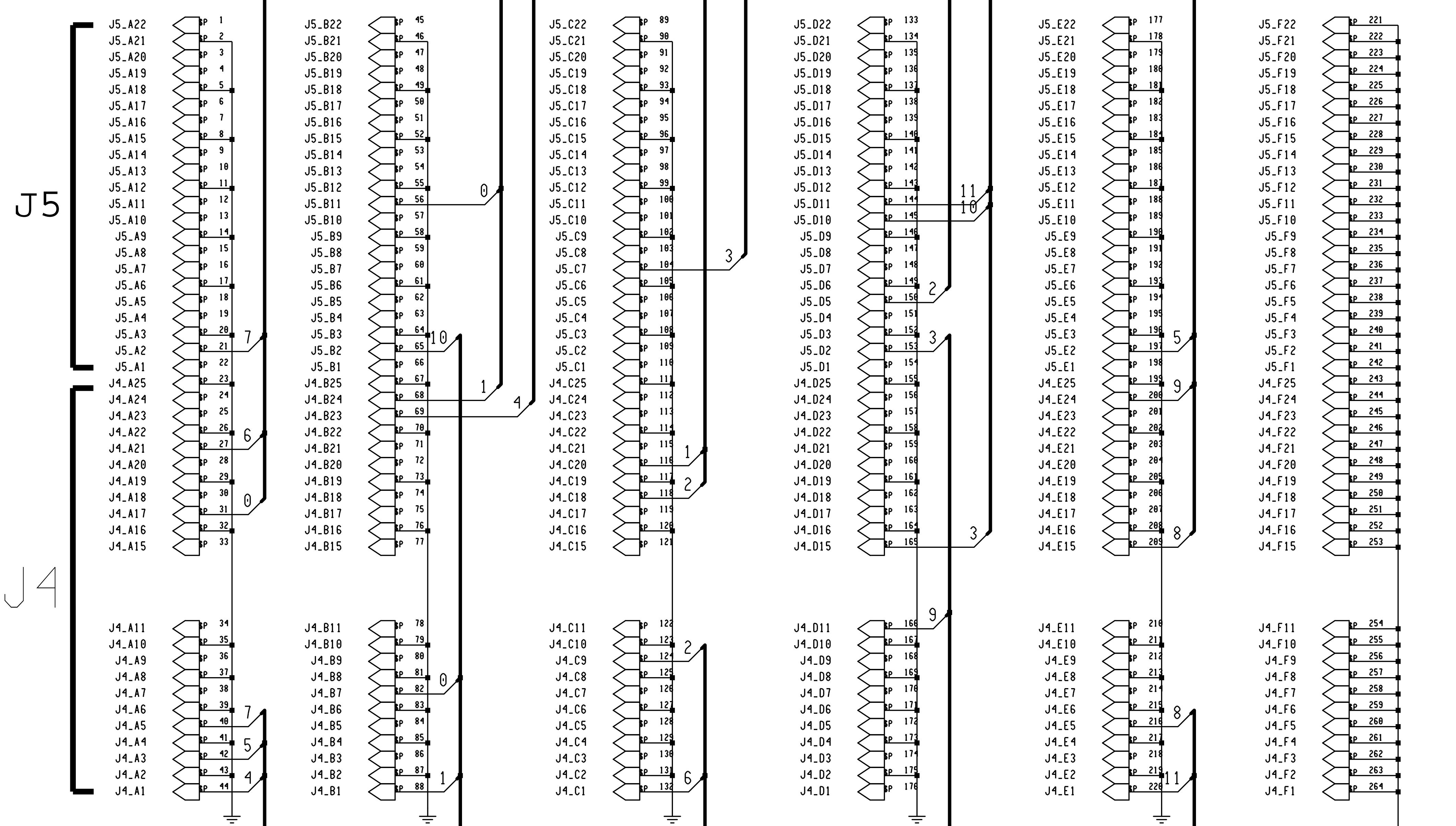


Static[11:0]
Clocks applied to CCDs during acquisition

Ctrl[7:0]
Clocks used to control the switches



Readout[11:0]
Clocks applied to CCDs during Readout

FPGND connects to the two screws holding the module's front panel.

Engineer	M. Bogdan	The University of Chicago 5640 S. Ellis Ave. Chicago, IL 60637	
Drawn by	M. Bogdan		
R&D CHK		J5 - J4 MUX Transition Board Dark Matter Search	
DATE:	4/12/13		
TIME:	11:17 am	REV A DRW. 2814 Sheet 2	
QA CHK			