3. "+3.3V" and "ground" are digital power and ground planes, common for all modules.

4. "+5Vopto", "-5Vopto" and "GNDopto" are local power and ground planes common for MMIC, PhSw and HKeeper Modules.

BLVDS inputs: SCK, CLR, DIN are doubled inside the Slot 8 SAB.

Notes:
2. All P2 pins feed through and do not connect to the Backplane.

3. "+3.3V" and "ground" are digital power and ground planes, common for all modules.

4. "+5Vopto", "-5Vopto" and "GNDopto" are local power and ground planes common for MMIC, PhSw and HKeeper Modules.

5. All BLVDS signals are driven each by an DS92001 Buffer, located on the Slot 8 SAB and are terminated on the Backplane. They shall not be terminated on Modules. BLVDS inputs: SCK, CLR, DIN are doubled inside the Slot 8 SAB.

6. All LVDS inputs are point-to-point and have to be terminated on Modules.