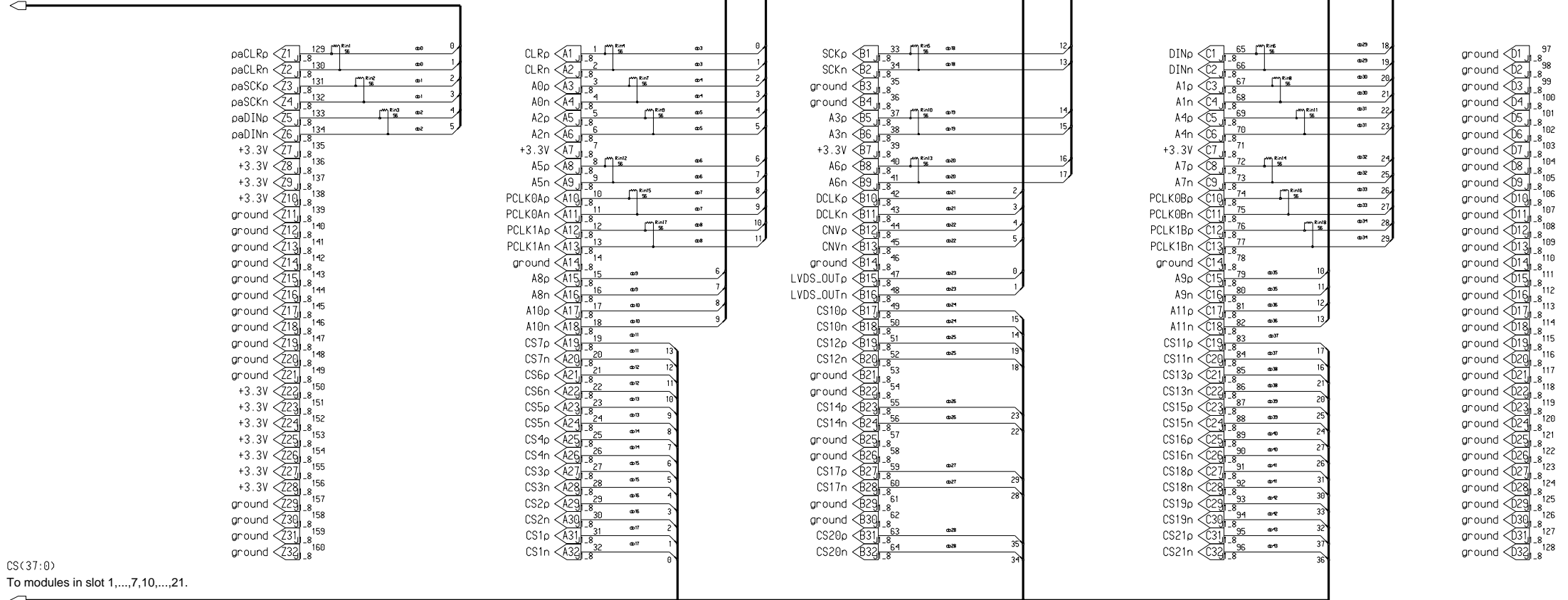


# P1 LVDS Input Connector

BLVDS\_9\_21(29:0)  
To modules in slot 9,....21.

LVDS(13:0)  
To HKeeper module only.

BLVDS\_1\_7(5:0)  
To modules in slot 1,....7.



## Notes:

0. Each pin has a name (e.g. "SCKp" or "ground") and a number (e.g. "B9" or "D2").
1. Pins that have the same name on the same sheet are connected together on the backplane.
3. "+3.3V" and "ground" are digital power and ground planes, common for all Cards in the Backplane.

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Drawn by	M. Bogdan		
R&D CHK		TITLE	Size B
DATE:	6/24/05	<b>P1-LVDS Input</b> <b>QEB-Backplane</b>	
TIME:	2:00 pm		
QA CHK		REV A	DRW. B-2563   Sheet 3 of 7