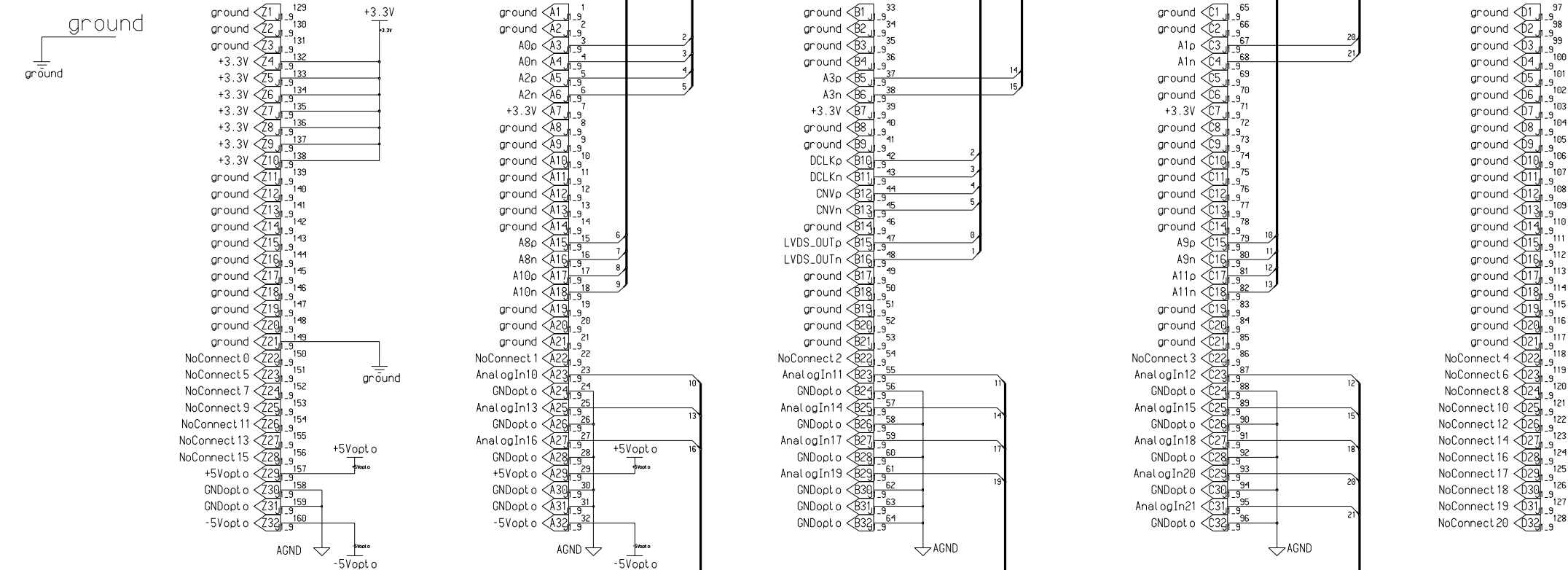


P1 - HKeeper

BLVDS(29:0)
From Connector in Slot 8.

LVDS(13:0)
From Connector in Slot 8.



AnalogIn(21:10)
From Modules in Slot 10,...,21.

Notes:

- Each pin has a name (e.g. "SCKp" or "ground") and a number (e.g. "B9" or "D2").
- Pins that have the same name on the same sheet are connected together on the backplane.
- Pins named NoConnect1,2,3,... are not connected to the backplane.
- " +3.3V" and "ground" are digital power and ground planes, common for all Modules in Backplane.
- " +5Vopto", "-5Vopto" and "GNDopto" are local power and ground planes common for MMIC, PhSw and HK Modules.
- Differential signals: "A0", "A1", "A2", "A3" are BLVDS, driven each by an DS92001 (B/LVDS-BLVDS Buffer) and are terminated on Backplane. They shall not be terminated on Module.
- "A8", "A9", "A10", "A11", "DCLK", "CNV" are LVDS and have to be terminated on Module.

Engineer	M. Bogdan	The University of Chicago	
Drawn by	M. Bogdan	5640 S. Ellis Ave. Chicago, IL 60637	
R&D CHK		TITLE	Size B
DATE:	6/24/05	P1-Housekeeper QEB-Backplane	
TIME:	2:00 pm		
QA CHK		REV A	DRW. B-2563 Sheet 4 of 7