P1 MMIC & PhSw

+3.3V

ground

BLVDS(29:0)

CS(1:0)

AnalogOut

+5Vopto
-5Vopto

AGND
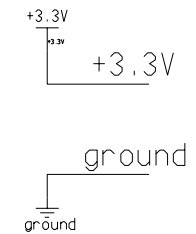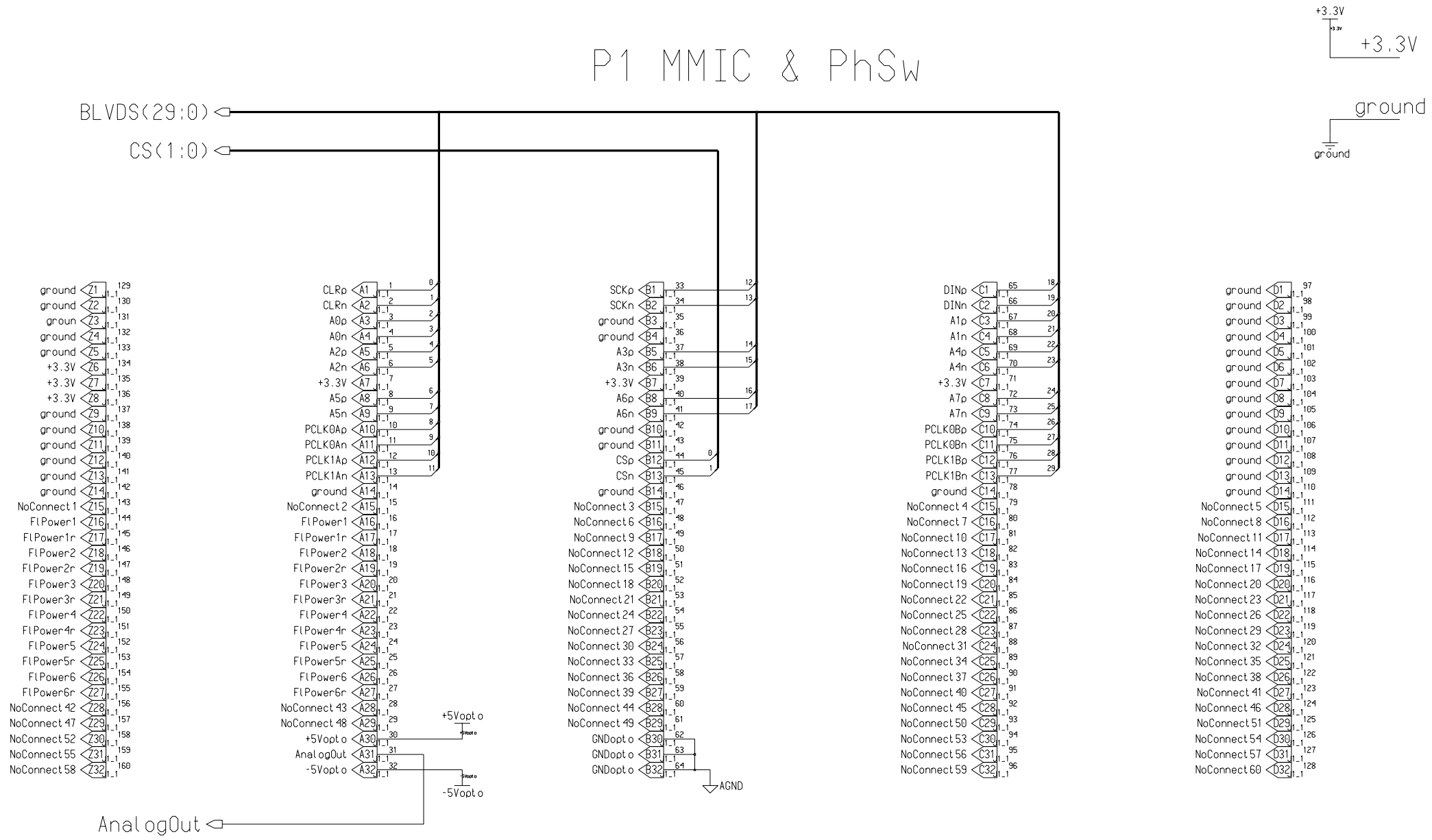
Notes:

0. Each pin has a name (e.g. "SCKp" or "ground") and a number (e.g. "B9" or "D2").

1. Pins that have the same name on the same sheet are connected together on the backplane; e.g. pinZ21 with A21 or Z16 with A16.

2. Pins named NoConnect1,2,3,... are not connected to the backplane.

3. "+3.3V" and "ground" are digital power and ground planes, common for all Modules in Backplane.

4. "+5Vopto", "-5Vopto" and "GNDopto" are local power and ground planes common for MMIC, PhSw and HK Modules.

5. Differential signals in the bus BLVDS(29:0): "A0", "A1", "A2", "A3",... are BLVDS,
   driven each by an DS92001 (B/LVDS-BLVDS Buffer) and are terminated on Backplane.
   They shall not be terminated on Module.

6. "CS" is point-to-point LVDS and has to be terminated on Module.