

	1	2	3	4																									
A	ground <A1 ground <A2 ground <A3 +3.3V <A4 +3.3V <A5 +3.3V <A6 +3.3V <A7 +3.3V <A8 +3.3V <A9 +3.3V <A10 ground <A11 ground <A12 ground <A13 ground <A14 ground <A15 ground <A16 ground <A17 ground <A18 ground <A19 ground <A20 ground <A21 NoConnect0 <A22 NoConnect5 <A23 NoConnect7 <A24 NoConnect9 <A25 NoConnect11 <A26 NoConnect13 <A27 NoConnect15 <A28 +5Vopto <A29 GNDopto <A30 GNDopto <A31 -5Vopto <A32	ground <A1 ground <A2 A0n <A3 A0n <A4 A2n <A5 A2n <A6 +3.3V <A7 ground <A8 ground <A9 ground <A10 ground <A11 ground <A12 ground <A13 ground <A14 A8p <A15 A8n <A16 A10p <A17 A10n <A18 ground <A19 ground <A20 ground <A21 NoConnect1 <A22 AnalogIn10 <A23 GNDopto <A24 AnalogIn13 <A25 GNDopto <A26 AnalogIn16 <A27 GNDopto <A28 +5Vopto <A29 GNDopto <A30 GNDopto <A31 -5Vopto <A32	ground <B1 ground <B2 ground <B3 ground <B4 A3p <B5 A3n <B6 +3.3V <B7 ground <B8 ground <B9 DCLKp <B10 DCLKn <B11 CNVp <B12 CNVn <B13 ground <B14 LVDS_OUTp <B15 LVDS_OUTn <B16 ground <B17 ground <B18 ground <B19 ground <B20 ground <B21 NoConnect2 <B22 AnalogIn11 <B23 GNDopto <B24 AnalogIn14 <B25 GNDopto <B26 AnalogIn17 <B27 GNDopto <B28 AnalogIn19 <B29 GNDopto <B30 GNDopto <B31 GNDopto <B32	ground <C1 ground <C2 A1p <C3 A1n <C4 ground <C5 ground <C6 +3.3V <C7 ground <C8 ground <C9 ground <C10 ground <C11 ground <C12 ground <C13 ground <C14 A9p <C15 A9n <C16 A11p <C17 A11n <C18 ground <C19 ground <C20 ground <C21 NoConnect3 <C22 AnalogIn12 <C23 GNDopto <C24 AnalogIn15 <C25 GNDopto <C26 AnalogIn18 <C27 GNDopto <C28 AnalogIn20 <C29 GNDopto <C30 AnalogIn21 <C31 GNDopto <C32	ground <D1 ground <D2 ground <D3 ground <D4 ground <D5 ground <D6 ground <D7 ground <D8 ground <D9 ground <D10 ground <D11 ground <D12 ground <D13 ground <D14 ground <D15 ground <D16 ground <D17 ground <D18 ground <D19 ground <D20 ground <D21 NoConnect4 <D22 NoConnect6 <D23 NoConnect8 <D24 NoConnect10 <D25 NoConnect12 <D26 NoConnect14 <D27 NoConnect16 <D28 NoConnect17 <D29 NoConnect18 <D30 NoConnect19 <D31 NoConnect20 <D32	B																							
C	Notes: 1. This is the P1 pin list for the HKeeper Module in QEB. 3. "+3.3V" and "ground" are digital power and ground planes, common for all Modules in Backplane. 4. "+5Vopto", "-5Vopto" and "GNDopto" are local power and ground planes common for MMIC, PhSw and HK Modules. 5. Differential signals: "A0", "A1", "A2", "A3" are BLVDS, driven each by an DS92001 (B/LVDS-BLVDS Buffer) and are terminated on Backplane. They shall not be terminated on Module. 6. "A8", "A9", "A10", "A11", "DCLK", "CNV" are LVDS and have to be terminated on Module.				C																								
D	<table border="1" style="width: 100%; border-collapse: collapse;"> <tr> <td style="width: 20%;">Engineer</td> <td style="width: 20%;">M. Bogdan</td> <td colspan="2" style="text-align: center;">The University of Chicago 5640 S. Ellis Ave. Chicago, IL 60637</td> </tr> <tr> <td>Drawn by</td> <td>M. Bogdan</td> <td style="text-align: center;">TITLE</td> <td style="text-align: center;">Size B</td> </tr> <tr> <td>R&D CHK</td> <td></td> <td colspan="2" style="text-align: center;">P1 - HKeeper - Pin List QEB-Backplane</td> </tr> <tr> <td>DATE:</td> <td>6/4/05</td> <td colspan="2"></td> </tr> <tr> <td>TIME:</td> <td>2:00 pm</td> <td colspan="2"></td> </tr> <tr> <td>QA CHK</td> <td></td> <td>REV A</td> <td>DRW. B-xxxx Sheet x of n</td> </tr> </table>				Engineer	M. Bogdan	The University of Chicago 5640 S. Ellis Ave. Chicago, IL 60637		Drawn by	M. Bogdan	TITLE	Size B	R&D CHK		P1 - HKeeper - Pin List QEB-Backplane		DATE:	6/4/05			TIME:	2:00 pm			QA CHK		REV A	DRW. B-xxxx Sheet x of n	D
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