Notes:

1. This is the P1 pin list common for the MMIC and PhSw Modules in QEB.
2. The "FiPower" pins feed through and do not connect to the Backplane.
3. "+3.3V" and "ground" are digital power and ground planes, common for all Modules in Backplane.
4. "+5Vopto", "-5Vopto" and "GNDopto" are local power and ground planes common for MMIC, PhSw and HK Modules.
5. All differential signals, with the exception of "CS", are BLVDS, driven each by an DS92001 (B/LVDS-BLVDS Buffer) and are terminated on Backplane. They shall not be terminated on Modules.
6. "CS" is an LVDS signal (point-to-point) and it has to be terminated on Modules.