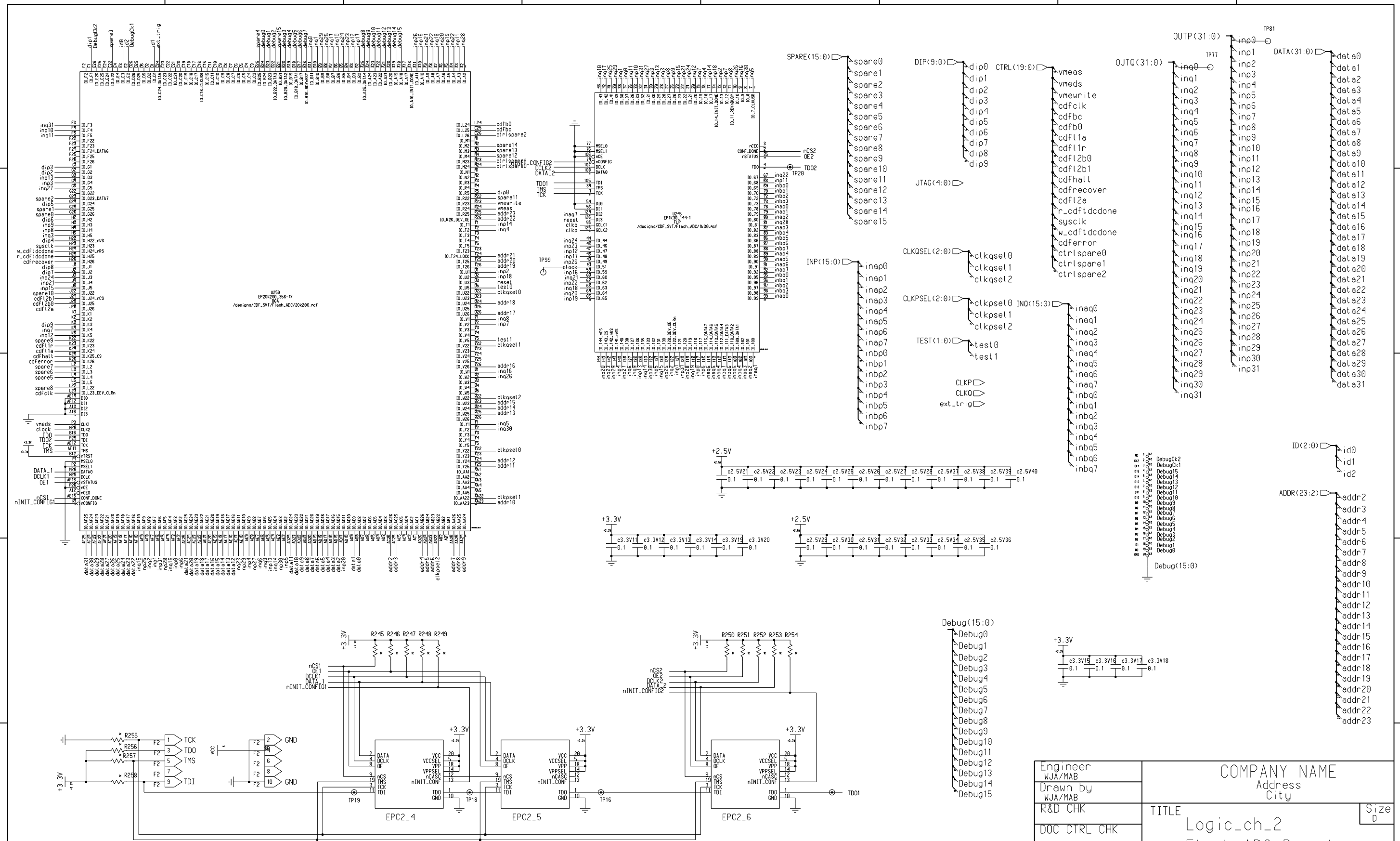
U59
EP20K200_356-1X
BGA
/designs/CDF_SVT/Flash_ADC/20k200.mcf
U245
EPIK30_144-1
PLP
/designs/CDF_SVT/Flash_ADC/1k30.mcf
SPARE(15:0)
DIP(9:0)
CTRL(19:0)
JTAG(4:0)
CLKQSEL(2:0)
CLKPSEL(2:0)
TEST(1:0)
INP(15:0)
INQ(15:0)
OUTP(31:0)
OUTQ(31:0)
DATA(31:0)
ID(2:0)
ADDR(23:2)
Debug(15:0)
EPC2_4
EPC2_5
EPC2_6
Engineer WJA/MAB
Drawn by WJA/MAB
R&D CHK
DOC CTRL CHK
MFG CTRL CHK
QA CHK
COMPANY NAME
Address
City
TITLE
Logic_ch_2
Flash_ADC_Board
Size D
REV
Drawing Number
Sheet 15 of 20
Changed by bogdan
Date Changed Tuesday, August 22, 2000
Time 10:43:38 am