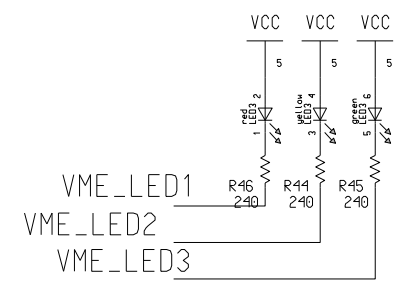
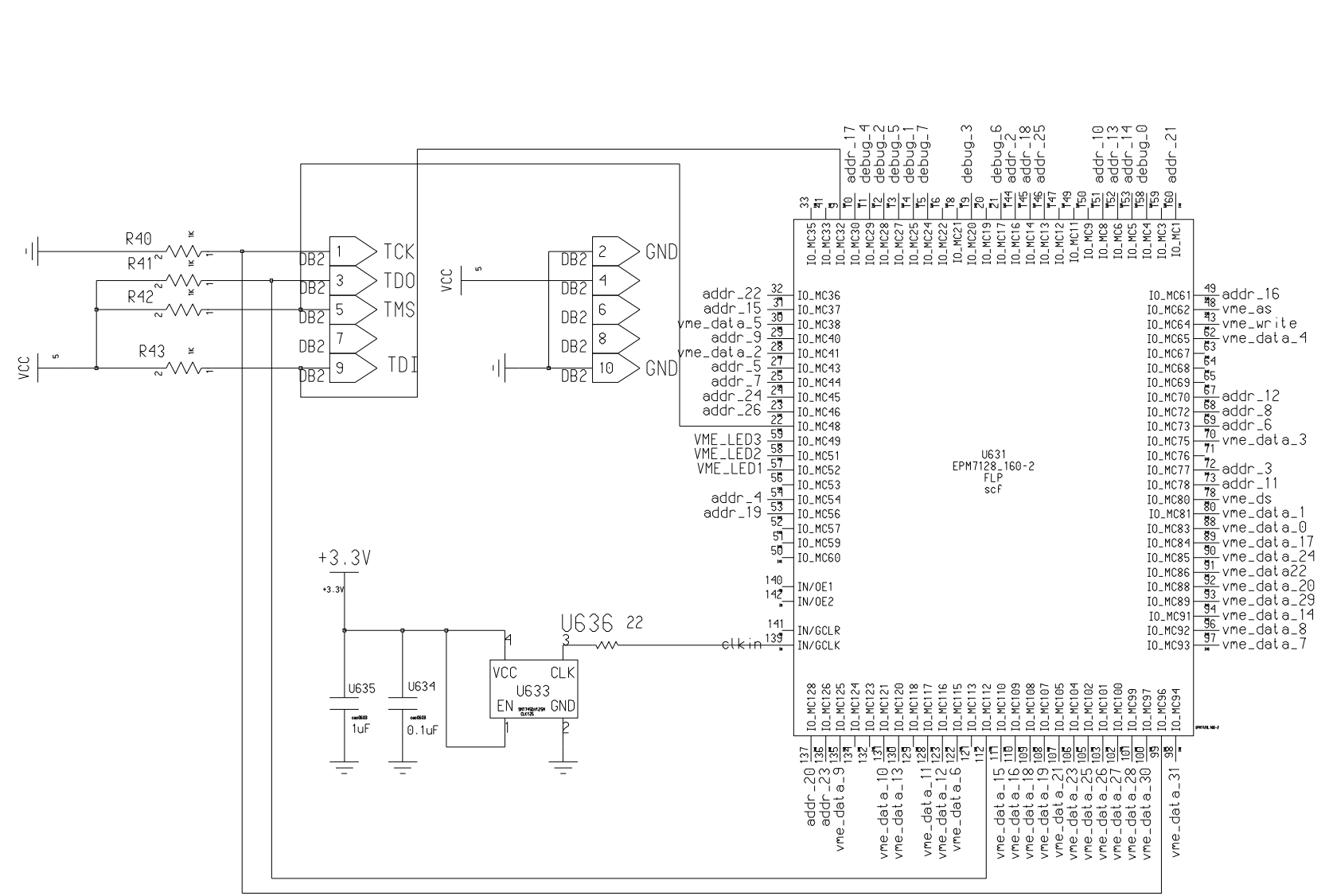


Data(31:0)

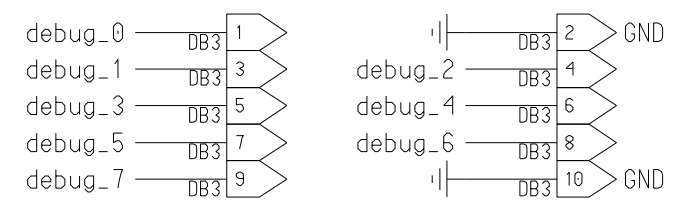
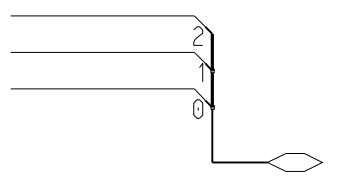
- vme_data_0
- vme_data_1
- vme_data_2
- vme_data_3
- vme_data_4
- vme_data_5
- vme_data_6
- vme_data_7
- vme_data_8
- vme_data_9
- vme_data_10
- vme_data_11
- vme_data_12
- vme_data_13
- vme_data_14
- vme_data_15
- vme_data_16
- vme_data_17
- vme_data_18
- vme_data_19
- vme_data_20
- vme_data_21
- vme_data_22
- vme_data_23
- vme_data_24
- vme_data_25
- vme_data_26
- vme_data_27
- vme_data_28
- vme_data_29
- vme_data_30
- vme_data_31



Local_VME_Addr(26:2)

- addr_2
- addr_3
- addr_4
- addr_5
- addr_6
- addr_7
- addr_8
- addr_9
- addr_10
- addr_11
- addr_12
- addr_13
- addr_14
- addr_15
- addr_16
- addr_17
- addr_18
- addr_19
- addr_20
- addr_21
- addr_22
- addr_23
- addr_24
- addr_25
- addr_26

vme_as
vme_ds
vme_write



Engineer: M. Bogdan
 Drawn by: M. Bogdan
 DATE: 9/1/11

The University of Chicago
FTK - VME Front Test Card
Local FPGA