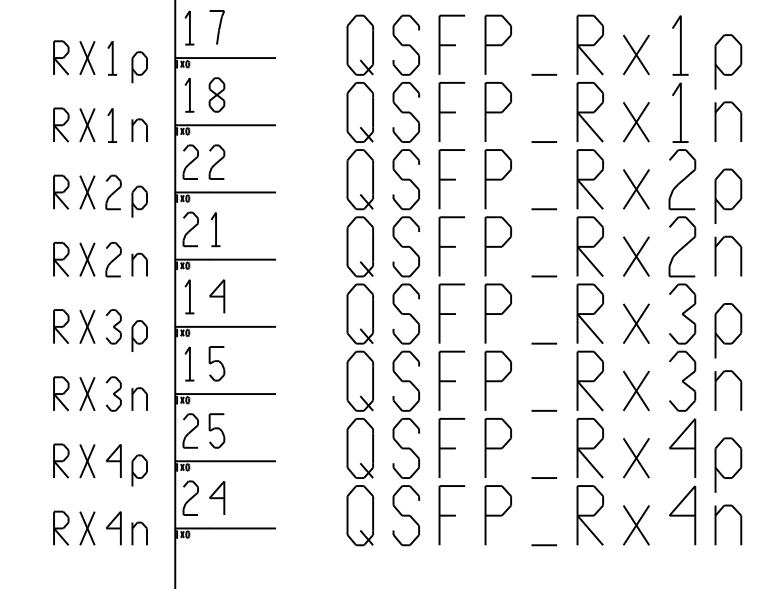
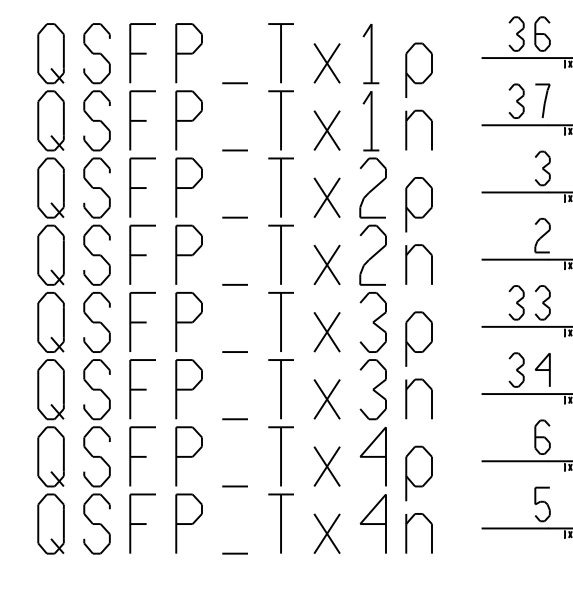
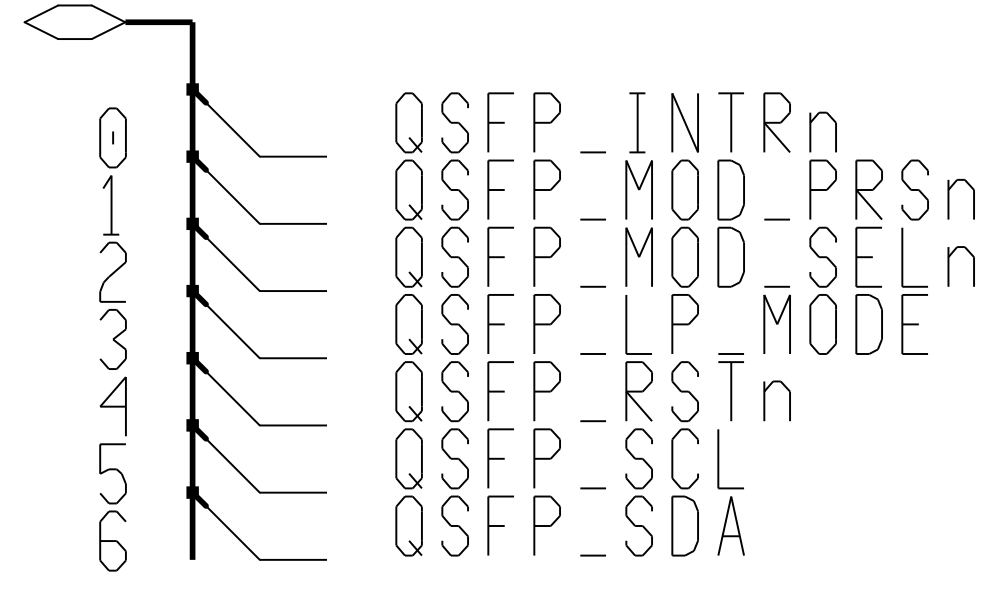
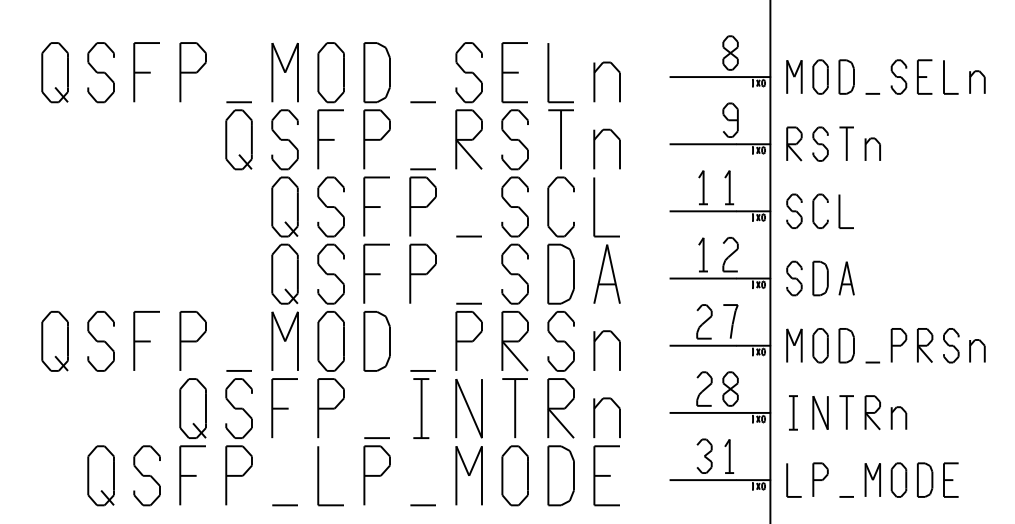
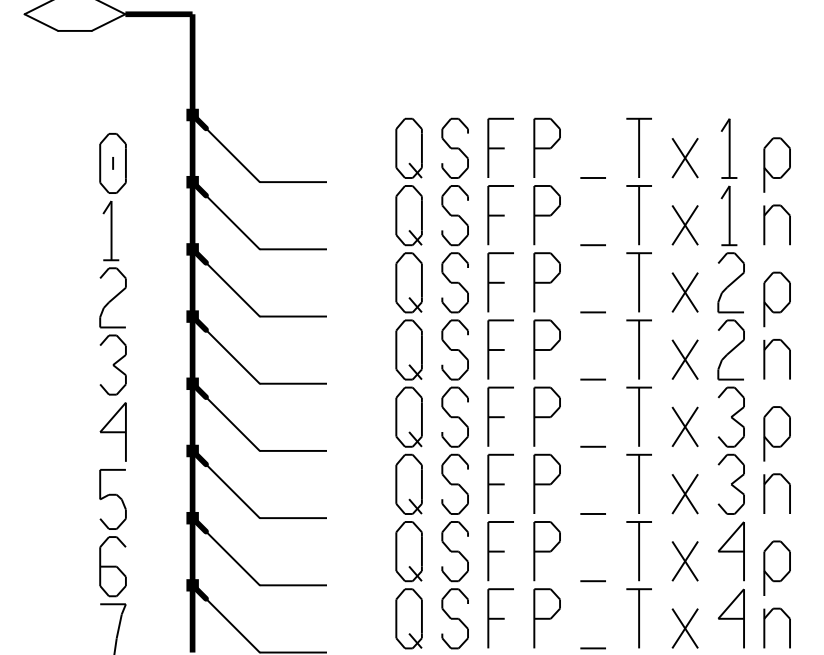


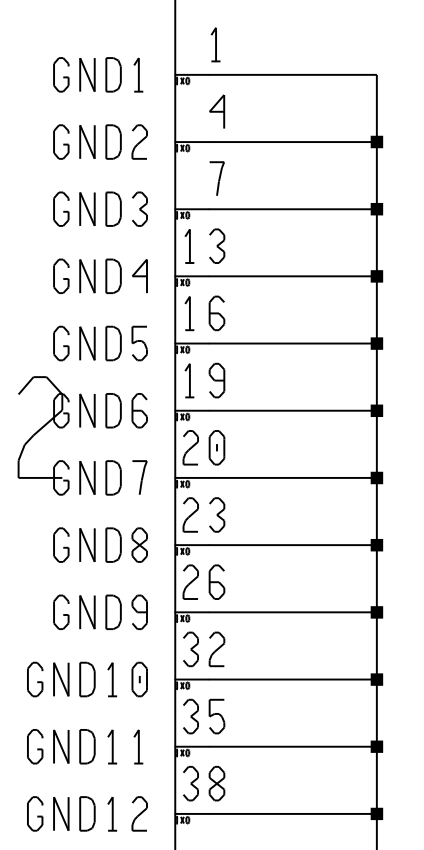
Control[6:0]



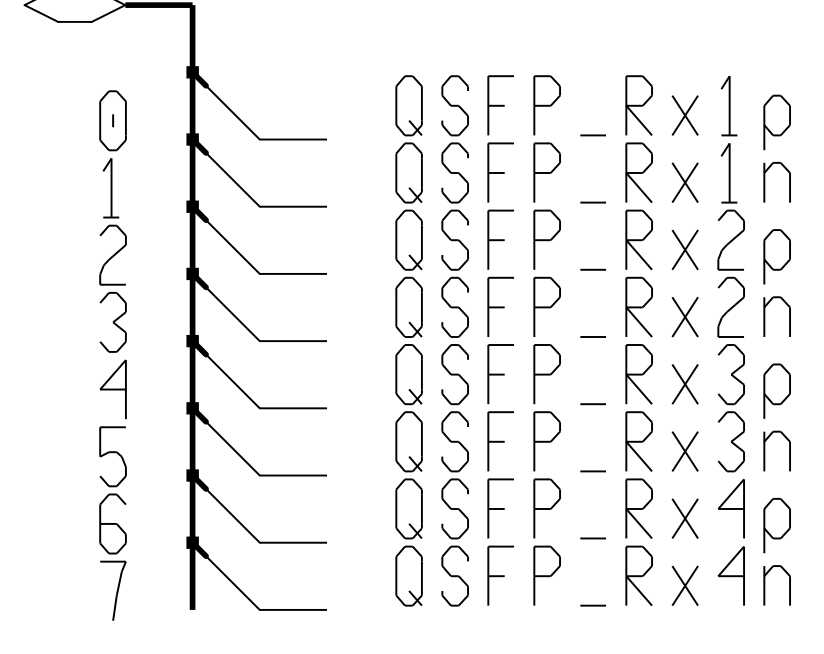
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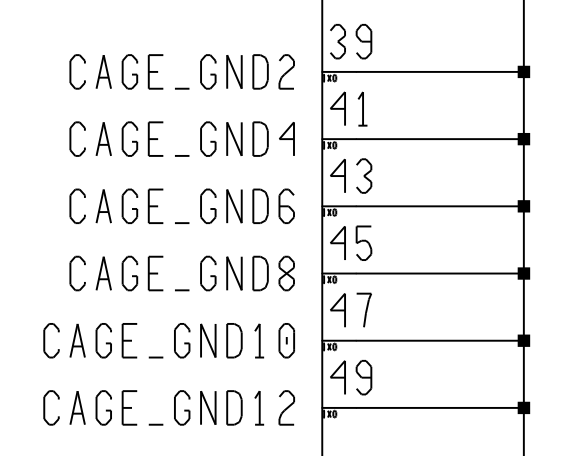
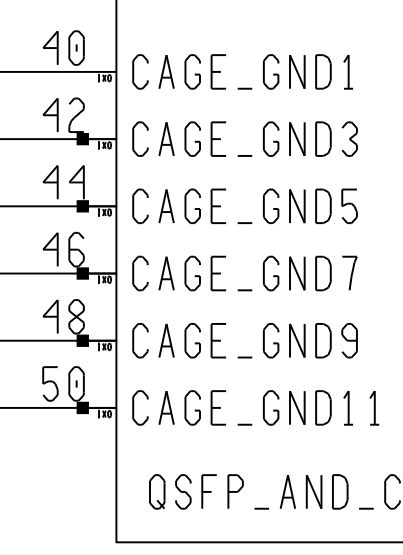
Q1\_12



Rx[7:0]

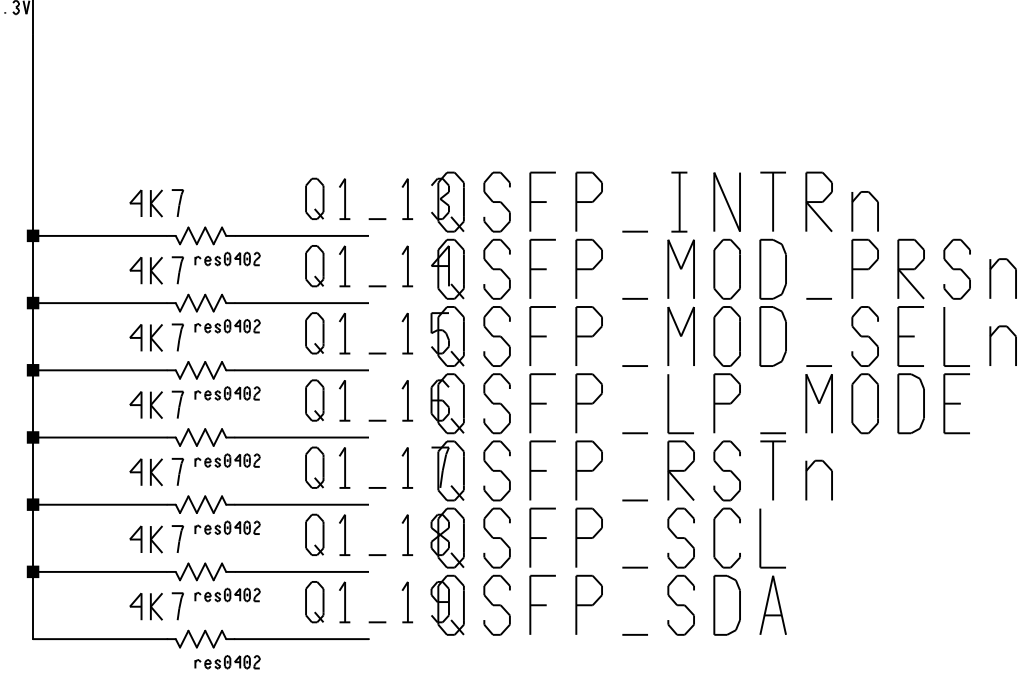


MTH1  
MTH2



**QSFP+ Module**  
**QSFP+ Cage**  
**iPASS R/A Connector, 38-pin**

+3.3V



Engineer	<b>M.Bogdan</b>	<b>The University of Chicago</b> 5640 S. Ellis Ave. Chicago, IL 60637	
Drawn by	<b>M.Bogdan</b>		
R&D CHK		TITLE	Size
DATE:	<b>12/25/12</b>	<b>QSFP Block</b> <b>FTK-AM-AUX Card</b>	1 c
TIME:	<b>11:17 am</b>		
REV	<b>A</b>	DRW.	<b>2808</b>
		<b>Sheet 18_1</b>	