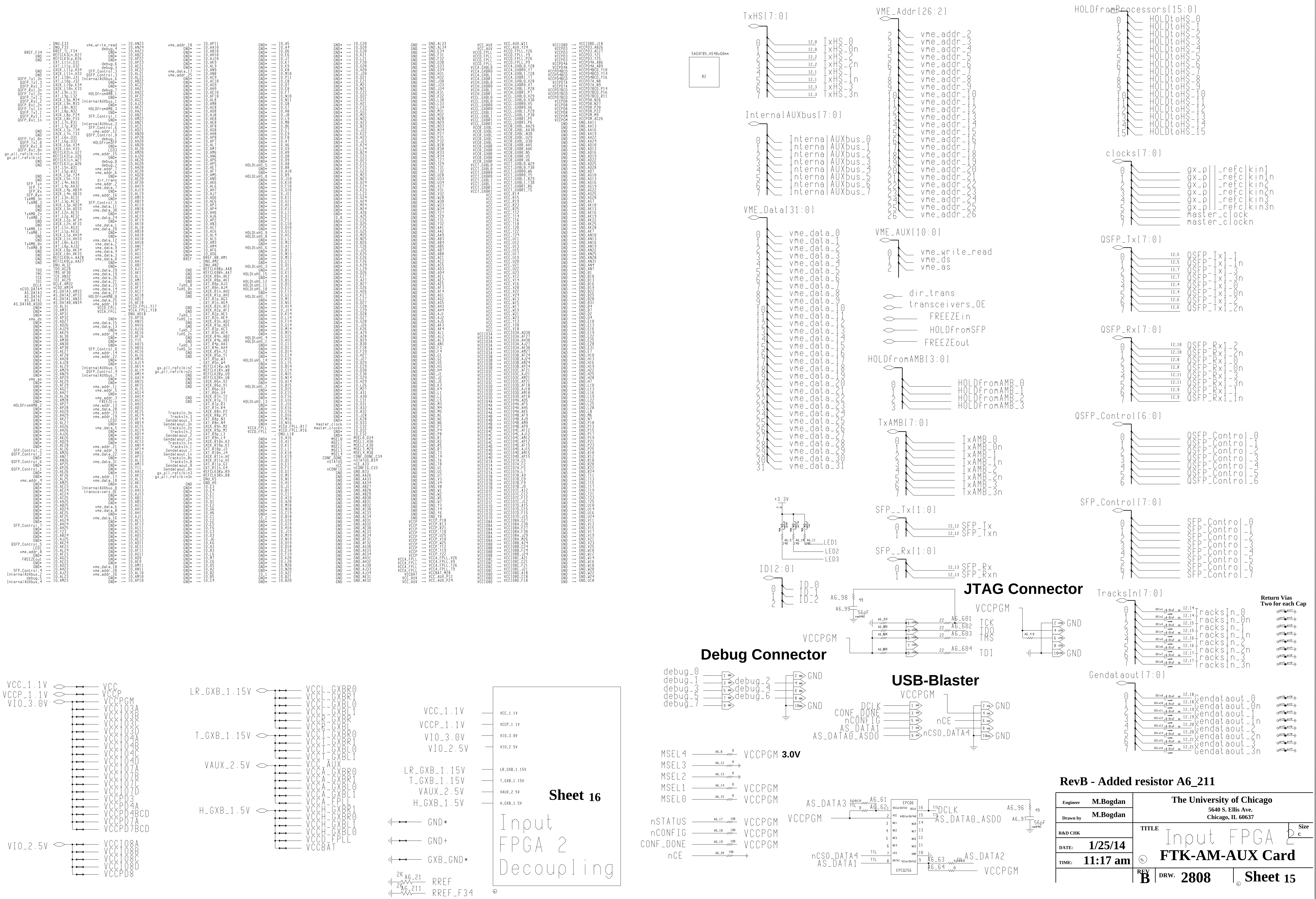

RevB - Added resistor A6_211

Engineer	M.Bogdan
Drawn by	M.Bogdan
R&D CHK	
DATE:	1/25/14
TIME:	11:17 am

The University of Chicago
5640 S. Ellis Ave.
Chicago, IL 60637

TITLE: Input FPGA 2

Size: C

FTK-AM-AUX Card

REV: **B** DRW. **2808** **Sheet 15**