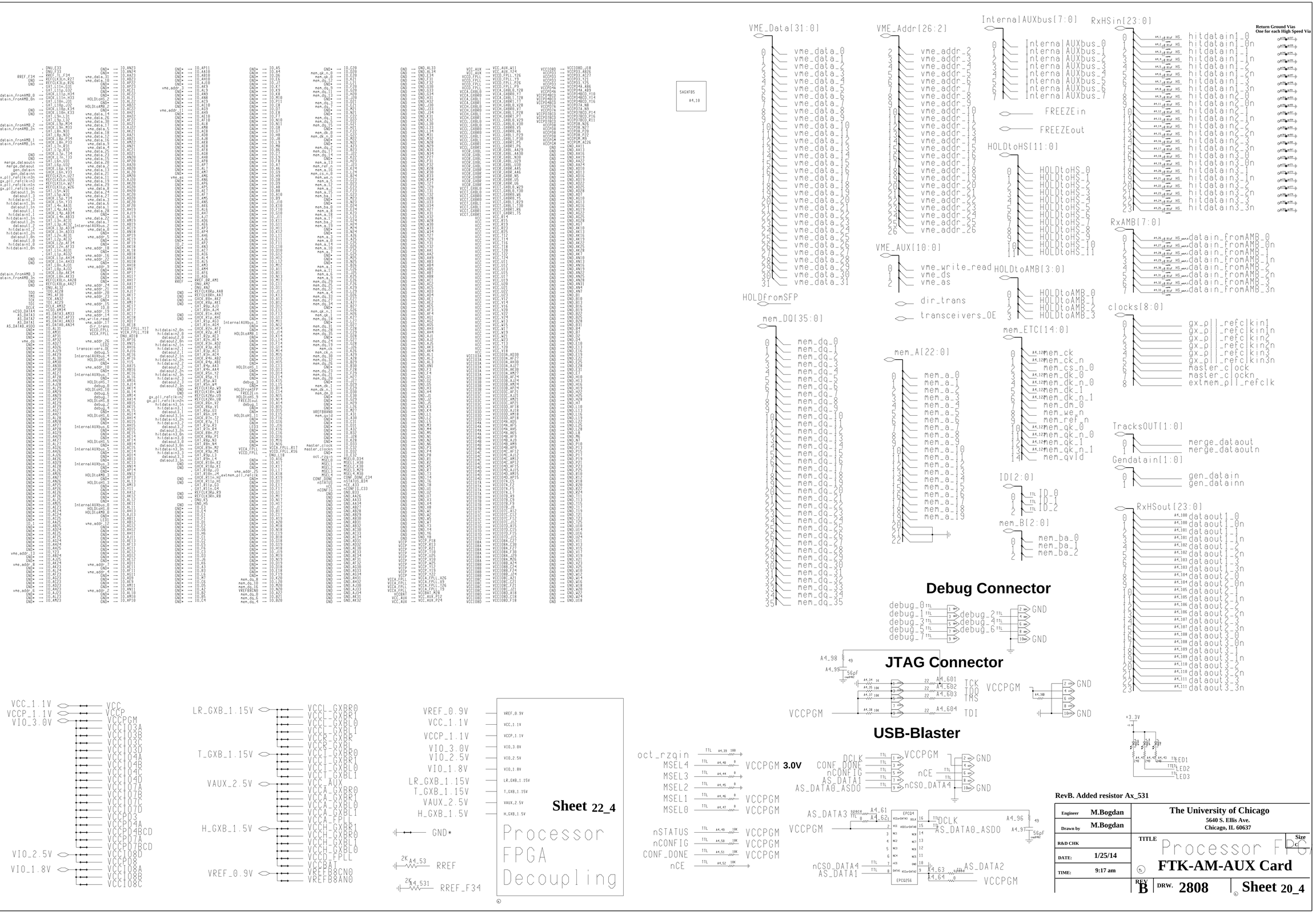

RevB. Added resistor Ax_531

Engineer	M.Bogdan	The University of Chicago 5640 S. Ellis Ave. Chicago, IL 60637	
Drawn by	M.Bogdan		
R&D CHK		TITLE: Processor FPGA	Size
DATE:	1/25/14	FTK-AM-AUX Card	
TIME:	9:17 am		
		REV. B DRW. 2808	Sheet 20_4