

IN[31:0]

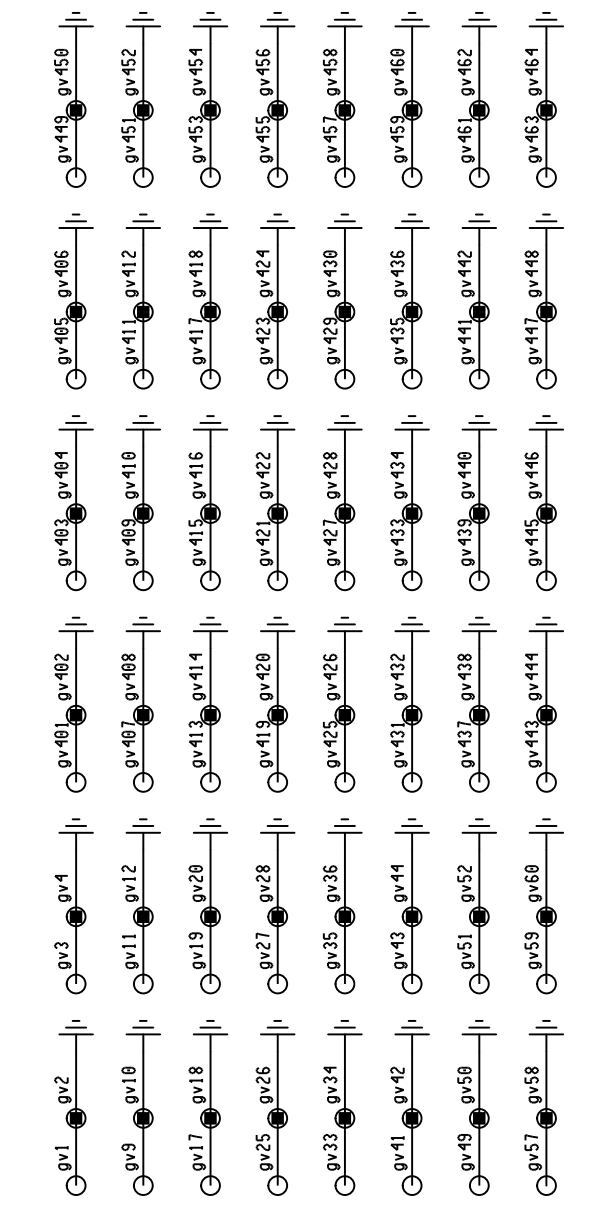
OUT[31:0]

Rx: From Front Module

Rx: To Local FPGA

Rev. B - Changed ripper numbers on Bus In[31:0]

Ground Vias



Engineer	M.Bogdan	TITLE	Size
Drawn by	M.Bogdan		C
R&D CHK			
DATE:	2/11/14		
TIME:	1:47 pm		
REV	B	DRW. 2808	Sheet 5

The University of Chicago
5640 S. Ellis Ave.
Chicago, IL 60637

Rx Buffers
FTK-AM-AUX Card