



IN[23:0]

Tx: to Front Module

Tx: From Local FPGA

Rev. B - Changed ripper numbers on bus OUT[23:0]

Engineer	M.Bogdan	The University of Chicago 5640 S. Ellis Ave. Chicago, IL 60637	Size C
Drawn by	M.Bogdan		
R&D CHK		TITLE	
DATE:	1/15/14	Tx Buffers	
TIME:	1:48 pm	FTK-AM-AUX Card	
REV	B	DRW.	2808 Sheet 6