



Engineer	M. Bogdan	The University of Chicago 5620 S. Ellis Ave. Chicago, IL 60637		
Drawn by	M. Bogdan			
R&D CHK		<i>16-Ch, GHz AC/DC ADC Module</i> Aux ADC Block		
DATE:	9/19/12			
TIME:	2:00 pm			
QA CHK		REV A	DRW. 2793	Sheet 10