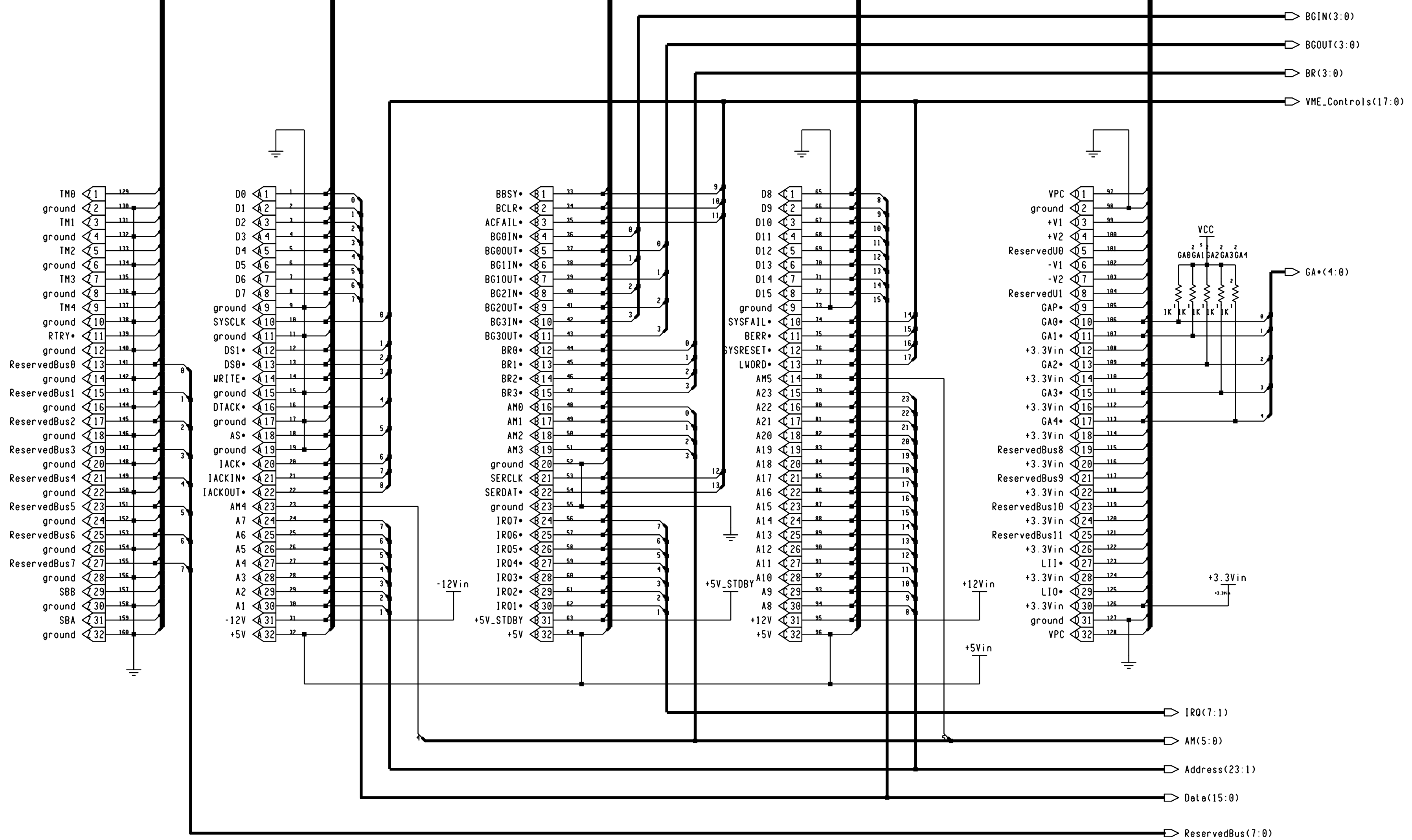


P1

VME\_P1(159:0)



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Drawn by	M. Bogdan		
R&D CHK		TITLE	Size C
DATE:	8/15/2012	16-Ch, GHz AC/DC ADC Module	
TIME:	2:00 pm	VME-P1	
QA CHK		REV A	DRW. 2693 Sheet 6