



**Not Used**  
BIDAC\_Control[5:0]

Engineer	M. Bogdan	<b>The University of Chicago</b> 5620 S. Ellis Ave. Chicago, IL 60637		
Drawn by	M. Bogdan			
R&D CHK		<i>16-Ch, GHz AC/DC ADC Module</i> <b>Voltage References</b>		
DATE:	<b>9/19/12</b>			
TIME:	2:00 pm			
QA CHK		REV A	DRW. <b>2793</b>	Sheet <b>9</b>