

4

3

DWG. NO. A - 2797 SH REV. A

1

D

D

C

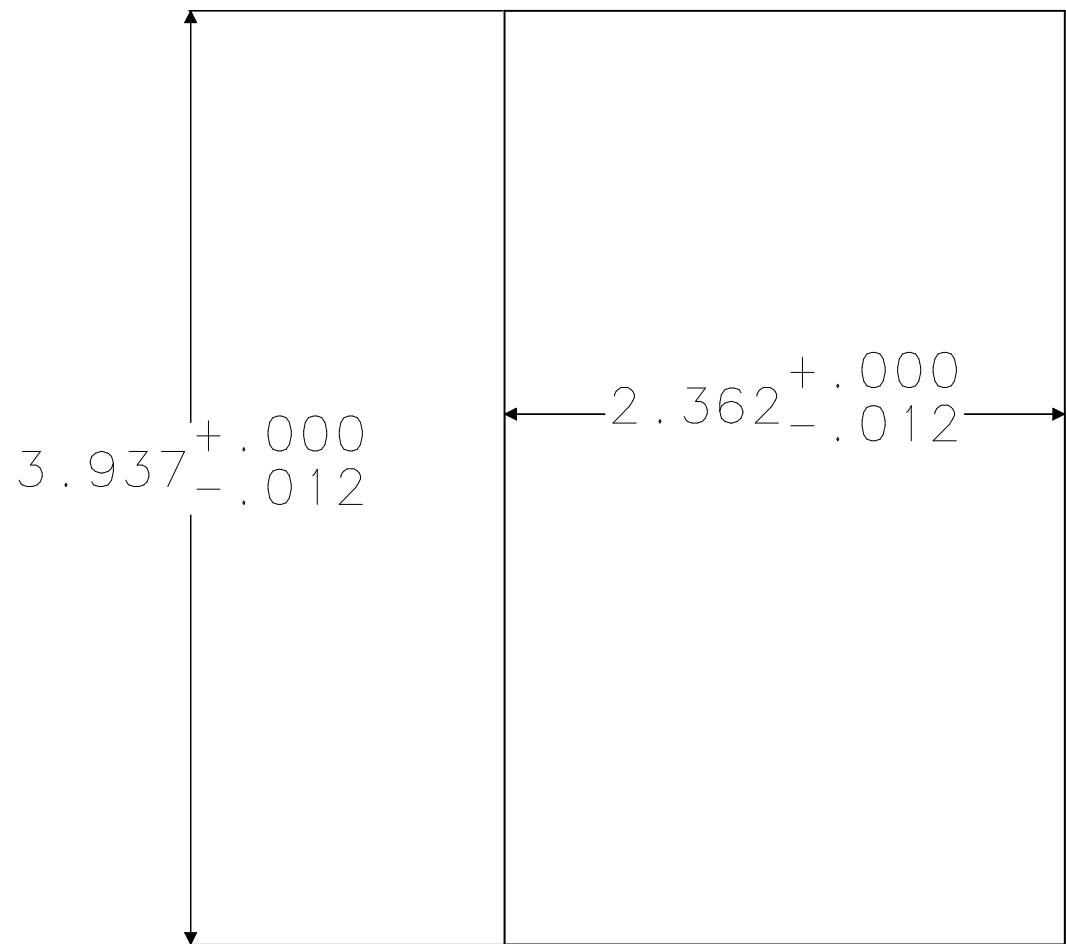
C

B

B

A

A



- Board Characteristics
- All dimensions are given in inches unless specified otherwise.
 - Minimum trace width: 0.005" on all layers
Minimum clearance: 0.005" on all layers
 - Impedance Control: 50 Ohm for all 5-mil signal traces;
 - 1 oz Copper for Top, Bottom, and all Power layers;
1/2 oz Copper for Stripline Trace layers;
 - Apply Solder Mask over bare copper.
 - Board Thickness: 0.063" +/- 0.008
 - Immersion Gold over Copper with min. Ni: 2.5-5 um; Au: 0.05-0.2 um;
 - Silkscreen on Component and Solder Sides.
 - FHS tolerances: +/- 0.003 unless specified otherwise.
 - Interlayer spacing as specified

BOARD'S DRILL SCHEDULE

DRILL SYMBOL	DRILL SIZE	COUNT	PLATED	Tolerance	COMMENT
○	.01	166	YES	---	
⊞	.011811024	12	YES	---	
⊙	.035	16	YES	---	
⊞	.041	43	YES	---	
⊙	.057	4	YES	---	
⊞	.062	4	YES	---	
⊙	.07	20	YES	---	
⊞	.104	4	YES	---	
	.12795276	4	YES	---	

UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES TOLERANCES ARE: FRACTIONS DECIMALS ANGLES .XX .XXX .XXX DO NOT SCALE DRAWING	CONTRACT NO.		UNIVERSITY OF CHICAGO ELECTRONICS DEVELOPMENT GROUP		
	APPROVALS	DATE	TITLE		
	DRAWN M. Bogdan	6/18/12	GHz AC/DC PreAmp Board Specification Drawing		
	CHECKED M. Bogdan	6/18/12	SIZE	FSCM NO.	DWG. NO. 2797
FINISH	ISSUED	SCALE 1/1		SHEET	
SIMILAR TO	ACT. WT	CALC WT			

4

3

2

1