6. TFM drives RESET_DONE = H.
1. TP places all drivers to/from TFM-S10Mx in hi-Z.

Power DOWN:
7. TP enables all drivers.
5. TP reads Main I2C and driven RSTx_IN.
4. TFM starts all power supplies, enables buffers to/from S10Mx, and drives PWRGD_OUT = H.
2. TFM generates 5V, 3.3V_PRE, 1.8V_PRE, enables MAX10/TP Bus and Main I2C Bus, RDY_OUT = H.
3. TFM disables buffers to/from S10Mx, stops all S10Mx power supplies and drives PWROFF_OUT = H.
2. TP drives PWREN_IN = L, PWRDWN_IN = H.

Power UP:
1. TP starts with all drivers in hi-Z, and applies 12V.
2. TFM generates 5V, 3.3V_PRE, 1.8V_PRE, enables MAX10/TP Bus and Main I2C Bus, RDY_OUT = H.
3. TP drives PWREN_IN = H, PWRDWN_IN = L, 12V_PGOOD_IN = H, TP keeps differential drivers in hi-Z.
4. TFM starts all power supplies, enables buffers to/from S10Mx, and drives PWRGD_OUT = H.
5. TP reads Main I2C and driven RSTx_IN.
6. TFM drives RESET_DONE = H.
7. TP enables all drivers.

NOTES:
1. TFM-LVDS Inputs Not Buffered. TP Must Keep All LVDS Outputs in hi-Z or GND until PWRGD_OUT = H.
2. TFM-Transceiver Inputs Not Buffered. TP Must Keep All Transceiver Outputs in hi-Z or <1Vpp until PWRGD_OUT = H.