**Board Characteristics**

- **16 Layer Board, Size:** 6.299" (+0.0/-0.012) by 9.187" (+0.0/-0.012)
- **All dimensions are given in inches unless specified otherwise.**
- **Material:** Megtron-6
- **0. All dimensions are given in inches unless specified otherwise.**
- **Zc** = 51 Ohm and **Zdiff** = 100 Ohm for all traces
- **1. Material:** Megtron-6
- **2. Minimum trace width:** 0.005" and clearance:** 0.005"**
- **3. 1 oz copper for all power layers and for Top and Bottom**
- **4. Zc** = 51 Ohm and **Zdiff** = 100 Ohm for all traces
- **5. Immersion Gold with min. Ni: 2.5-5 um; Au: 0.05-0.2 um.**
- **6. Board Thickness:** 0.093 +/- 0.008
- **7. Mill the Top and Bottom of board on the solder side to a thickness of 0.063" +/- 0.008**
- **8. Silkscreen on Component and Solder Sides.**
- **9. 45 degree chamfer.**
- **10. FHS tolerances:** +/- 0.002 unless specified otherwise.
- **11. Press Fit Holes with Specifications as per TE Conn. Application Specification 114-13219:**
- **12. Press Fit Holes with Specifications as per TE Conn. Application Specification 114-13219:**
- **13. Via in Pad - Via Fill and Overplate:**
- **14. Remove all non-functional inner layer pad for pins and vias.**
- **15. Do Not Change Trace Widths.**
- **16. Do Not Change Trace Widths.**

**Board’s Drill Schedule**

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<th>DRILL SYMBOL</th>
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**Note:**

- **0.095** /- 0.008
- **1.887** /- 0.012

**Layer Order**

1. Signal_1
2. Power
3. Signal_2
4. Power
5. Signal_3
6. Power
7. Signal_4
8. Power
9. Signal_5
10. Power
11. Signal_6
12. Power
13. Signal_7
14. Power
15. Signal_8
16. Power