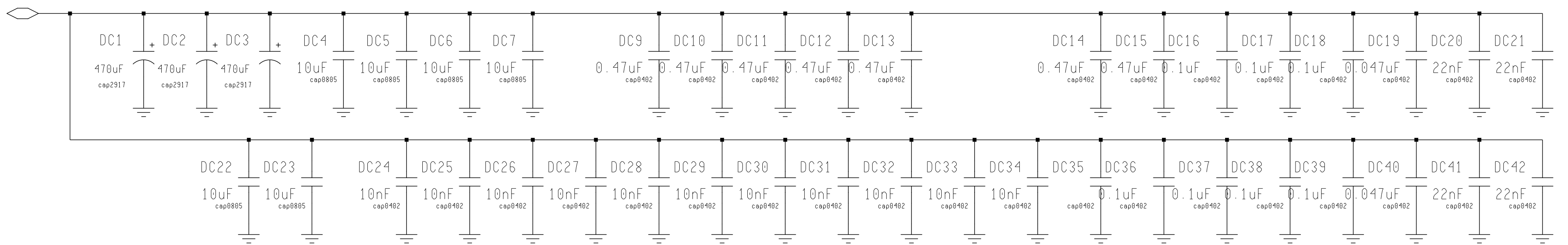
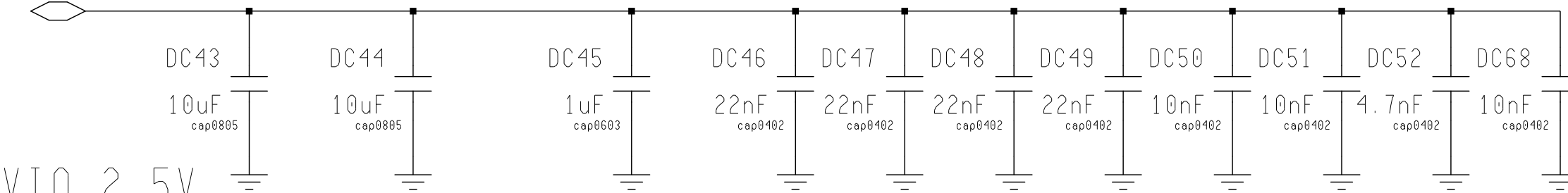


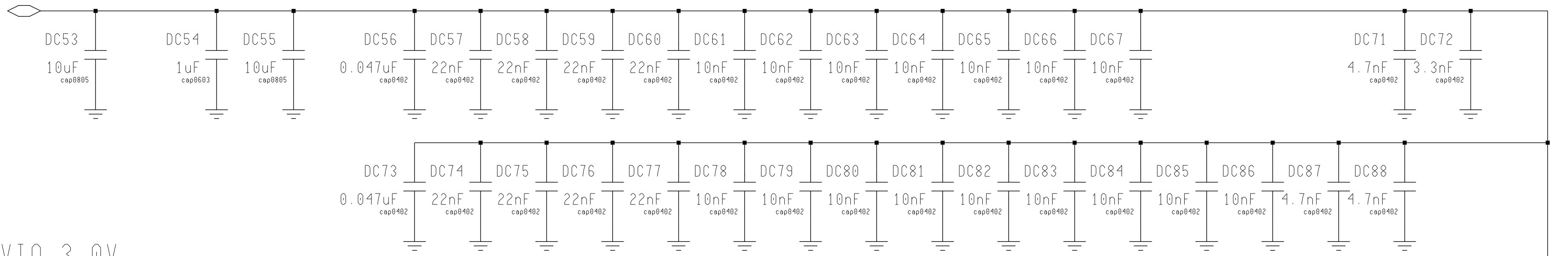
VCC_1.1V



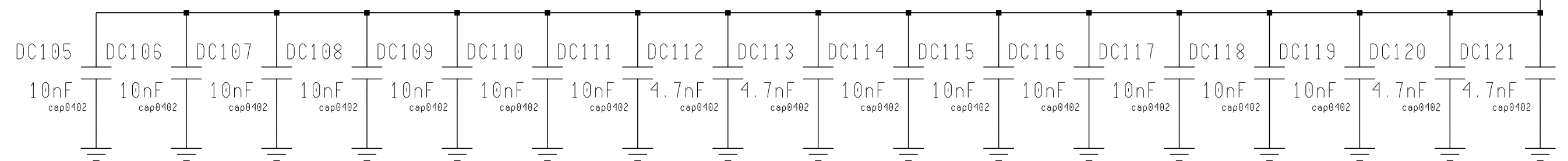
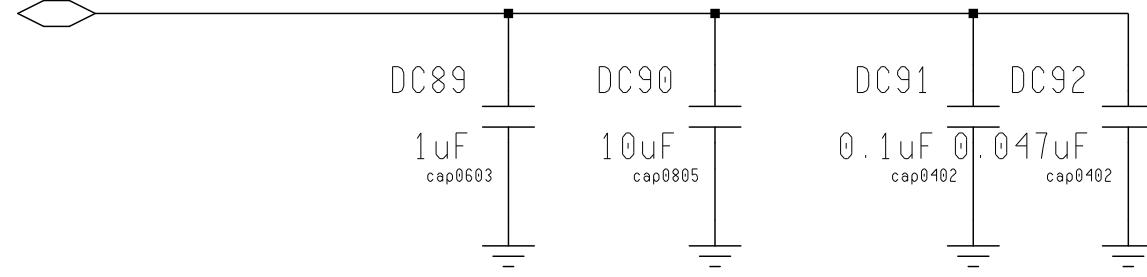
VCCP_1.1V



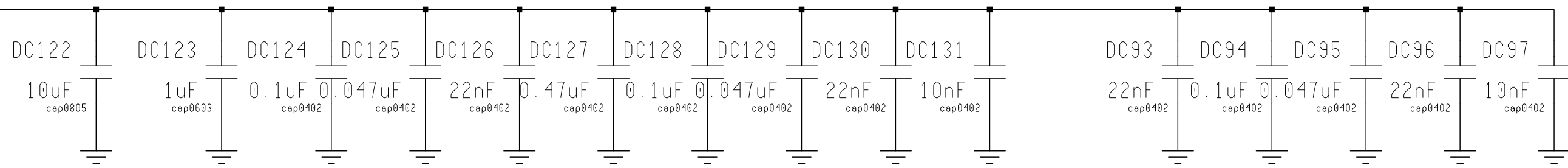
VIO_2.5V



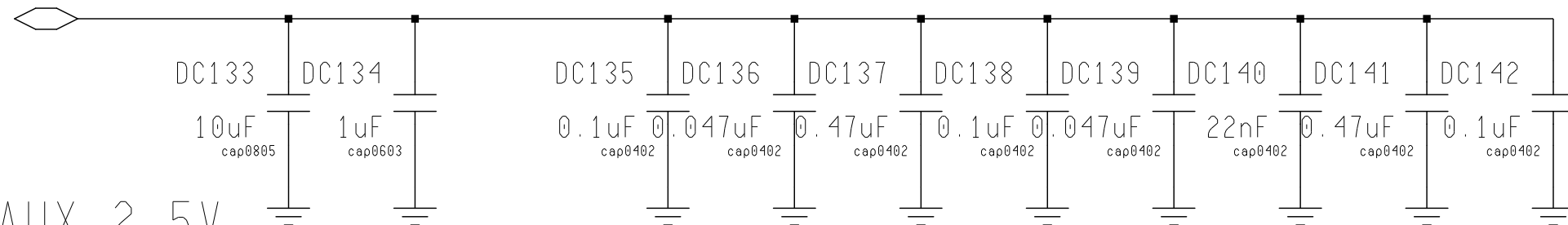
VIO_3.0V



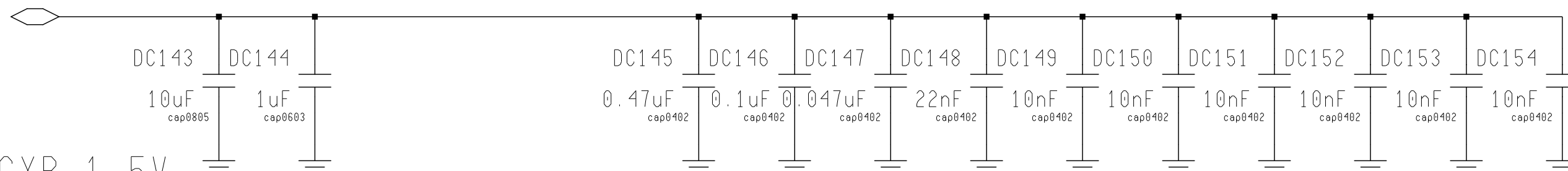
LR_GXB_1.15V



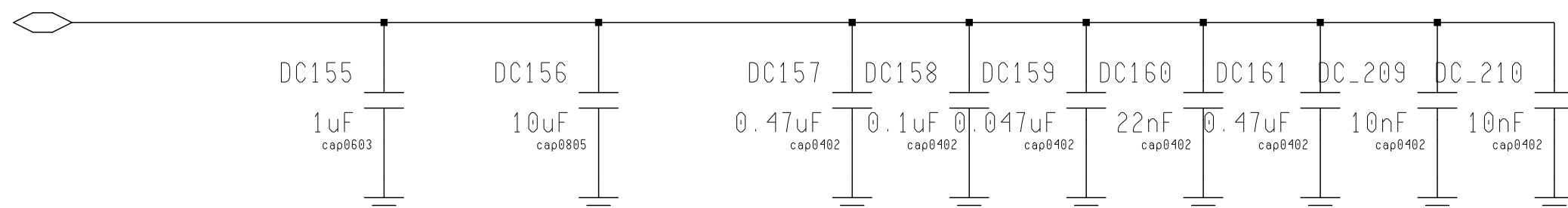
T_GXB_1.15V



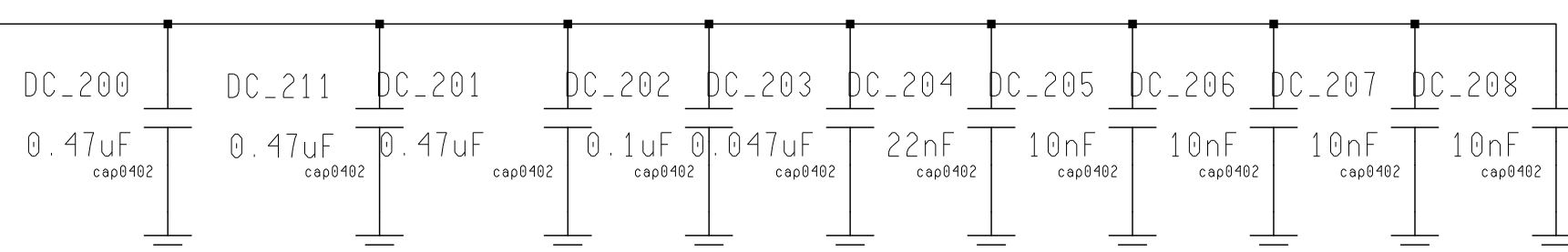
VAUX_2.5V



H_GXB_1.5V



VIO_1.8V



Engineer: M. Bogdan	<i>Optical Fiber Center Module</i> FPGA Decoupling Circuit The University of Chicago		
Drawn by: M. Bogdan			
DATE: 6/26/17			
SPC#2902 ASM#2903	REV. A	DRW. 2901	Sheet 7