

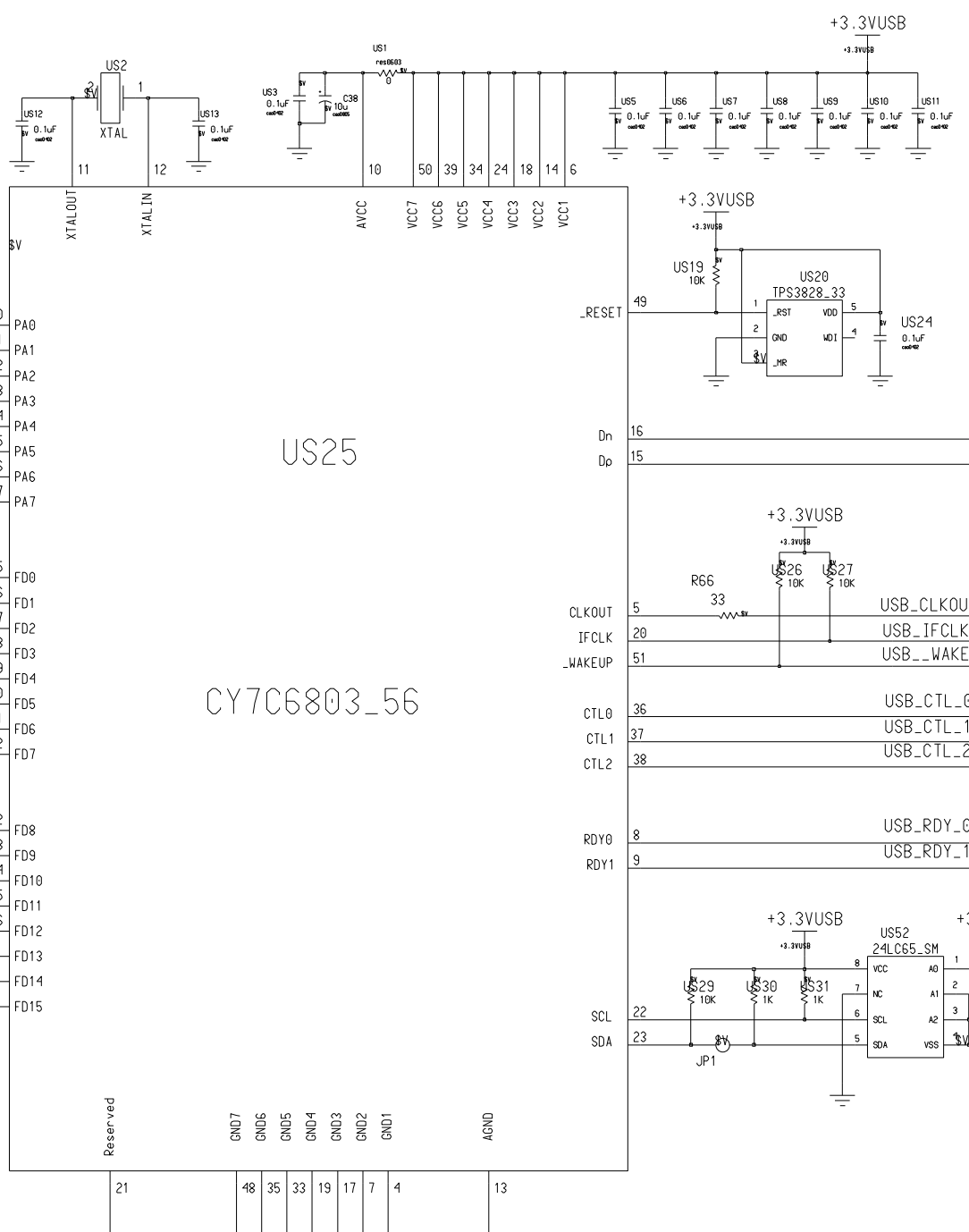
USB\_PA[7:0]

USB\_FD[15:0]

USB\_AUX[8:0]

CY7C6803\_56

US25



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Drawn by	M. Bogdan		
R&D CHK		TITLE	Size B
DATE:	8/16/2010	<b>PSEC2 Flip Chip ADC Module</b>	
TIME:	2:00 pm	<b>USB Interface</b>	
QA CHK		REV A	DRW. 2707 Sheet 2 of 5