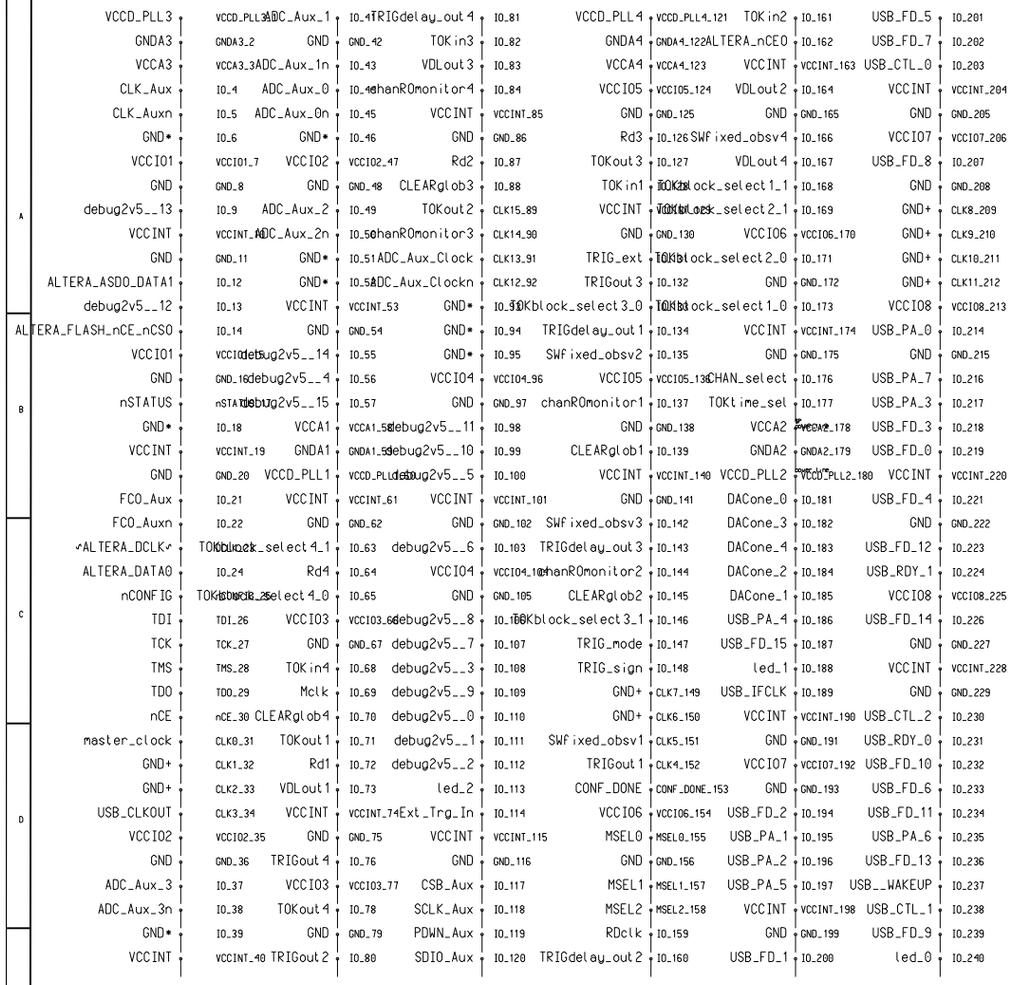
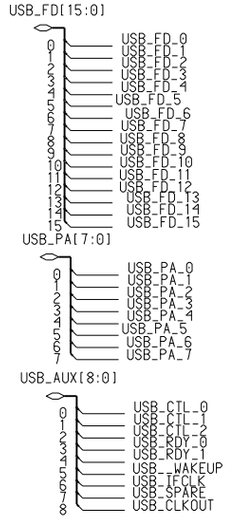
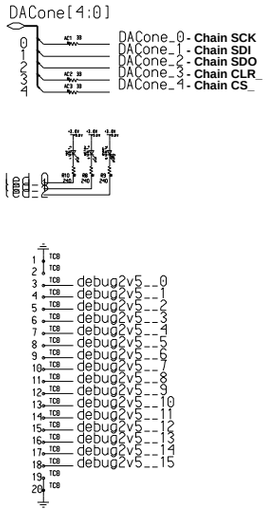
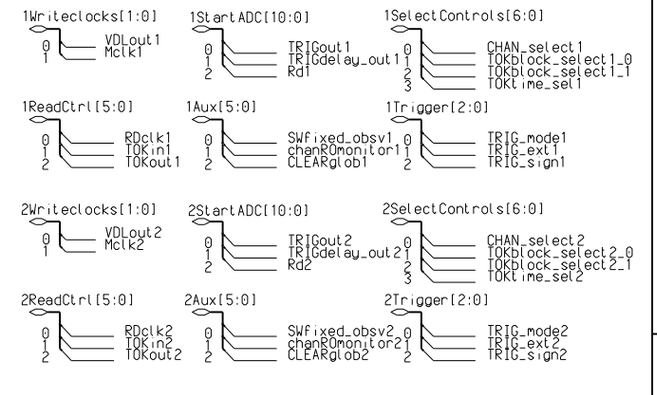
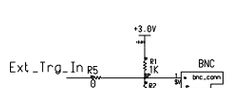


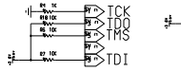


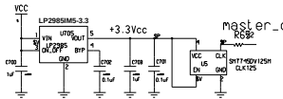


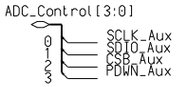


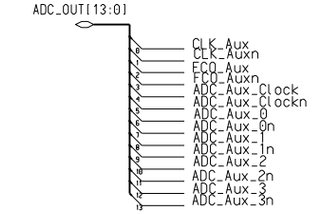


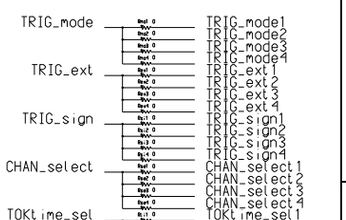
JTAG Connector

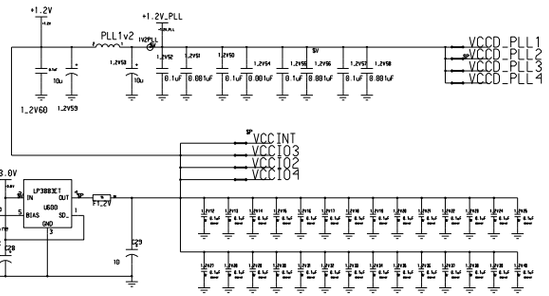


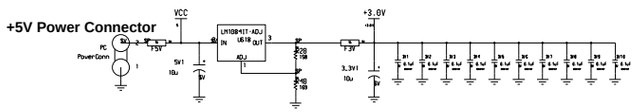




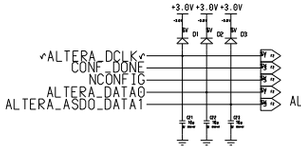


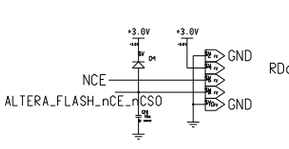


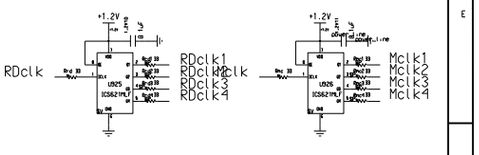


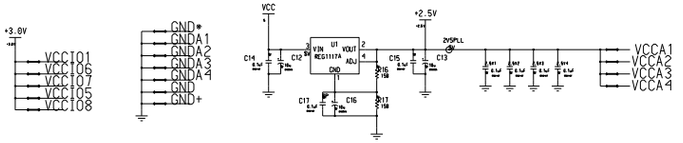


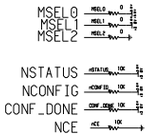
USB-Blaster

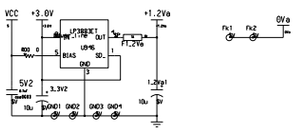












Engineer M.Bogdan	The University of Chicago 5640 S. Ellis Ave. Chicago, IL 60637
Drawn by M.Bogdan	
R&D CHK	TITLE: *PSEC2 Flip Chip ADC Module FPGA*
DATE: 8/16/2010	Size C
TIME: 2:00 pm	
QA CHK	REV A | DRW. 2707 | Sheet 5 of 7