PSEC3 QFP Mezzanine Card

Schematic Drawing: 2725 Rev A
Specification Drawing: 2726 Rev A
Assembly Drawing: 2727 Rev A
Feb. 22, 2011

The University of Chicago
Enrico Fermi Institute
Electronics Development Group
Film1 - SIGNAL_1, Top
PSEC3 QFP Mezzanine Card

SCHEMATIC DRAWING: 2725 Rev A
SPECIFICATION DRAWING: 2726 Rev A
ASSEMBLY DRAWING: 2727 Rev A
Feb. 22, 2011

The University of Chicago
Enrico Fermi Institute
Electronics Development Group

File2 - GROUND
PSEC3 QFP Mezzanine Card

SPECIFICATION DRAWING: 2725 Rev. A
ASSEMBLY DRAWING: 2727 Rev. A
Feb. 22, 2011

The University of Chicago
Enrico Fermi Institute
Electronics Development Group

Film3 - POWER 1: ±1.2V, ±3.0V
PSEC3 QFP Mezzanine Card

SCHEMATIC DRAWING: 2758 Rev A
SPECIFICATION DRAWING: 2756 Rev A
ASSEMBLY DRAWING: 2757 Rev A
Feb. 22, 2011

The University of Chicago
Enrico Fermi Institute
Electronics Development Group

Film 6 - SIGNAL 4, Bottom
PSEC3 QFP Mezzanine Card

The University of Chicago
Enrico Fermi Institute
Electronics Development Group

File 8 - Bottom Silkscreen
PSEC3 QFP Mezzanine Card

Schematic Drawing: 2725 Rev. A
Specification Drawing: 2726 Rev. A
Assembly Drawing: 2727 Rev. A
Feb. 22, 2011

The University of Chicago
Enrico Fermi Institute
Electronics Development Group

File9 - Top Soldermask
PSEC3 QFP Mezzanine Card

The University of Chicago
Enrico Fermi Institute
Electronics Development Group

Film10 - Bottom Soldermask