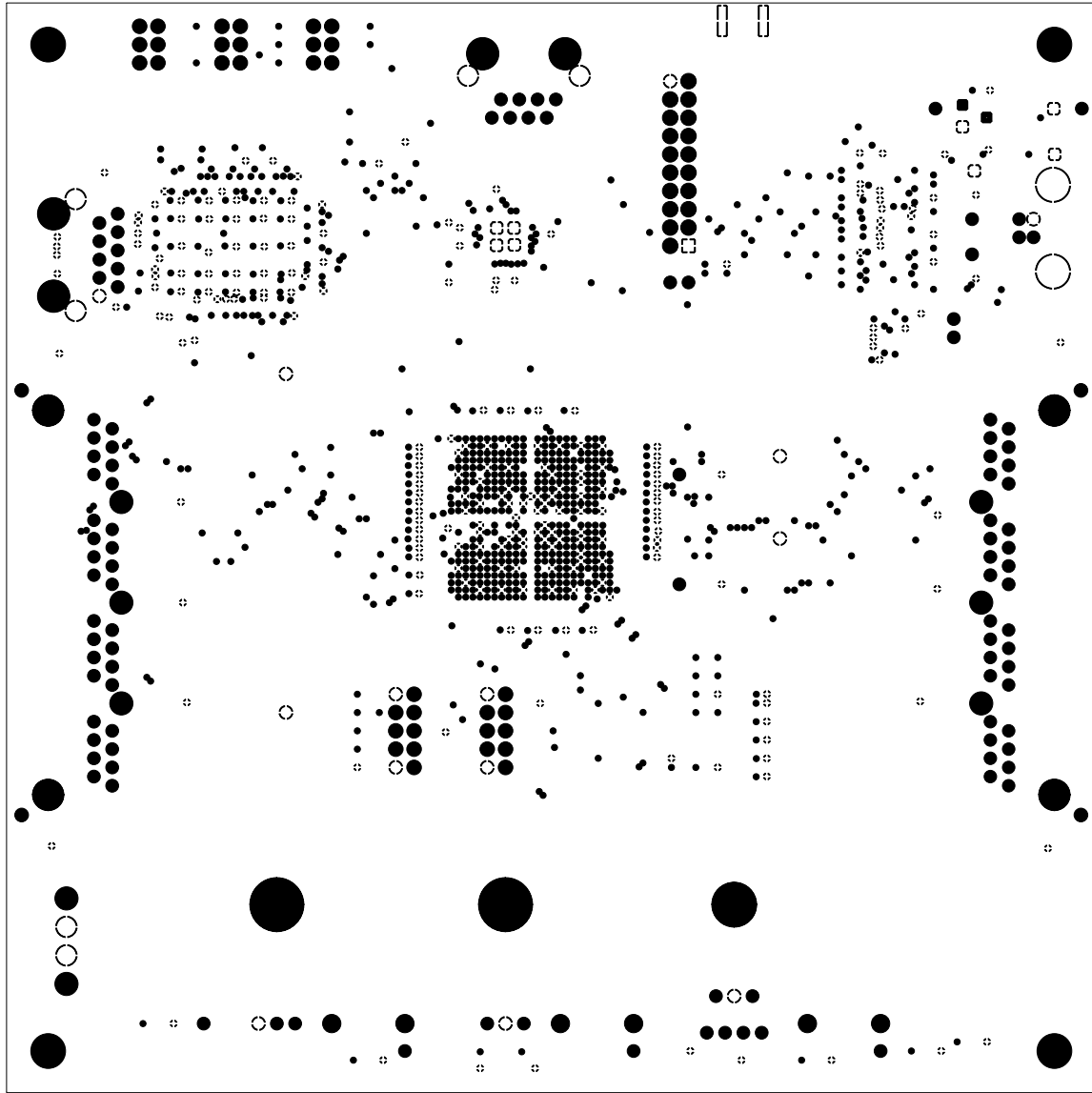
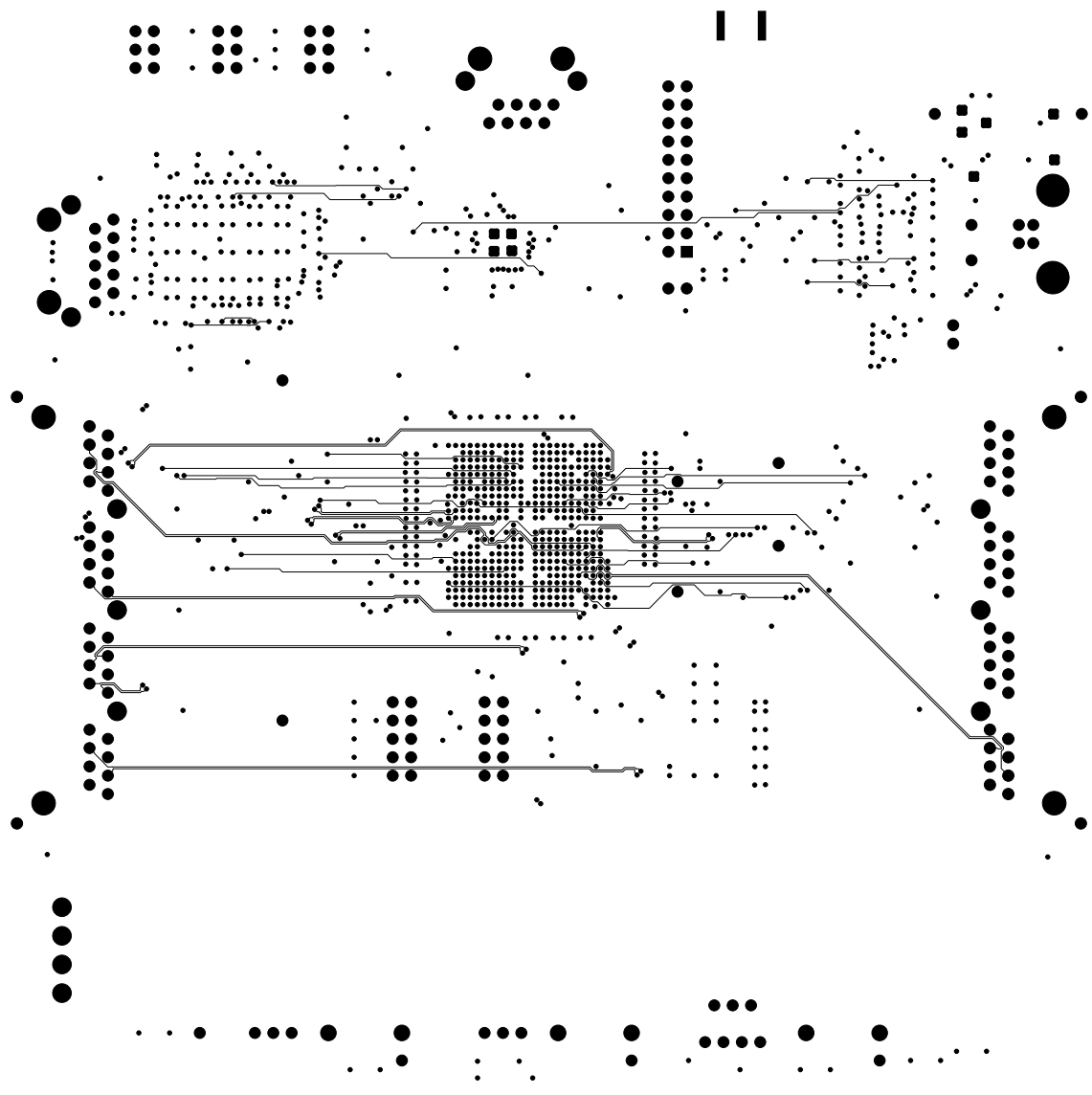


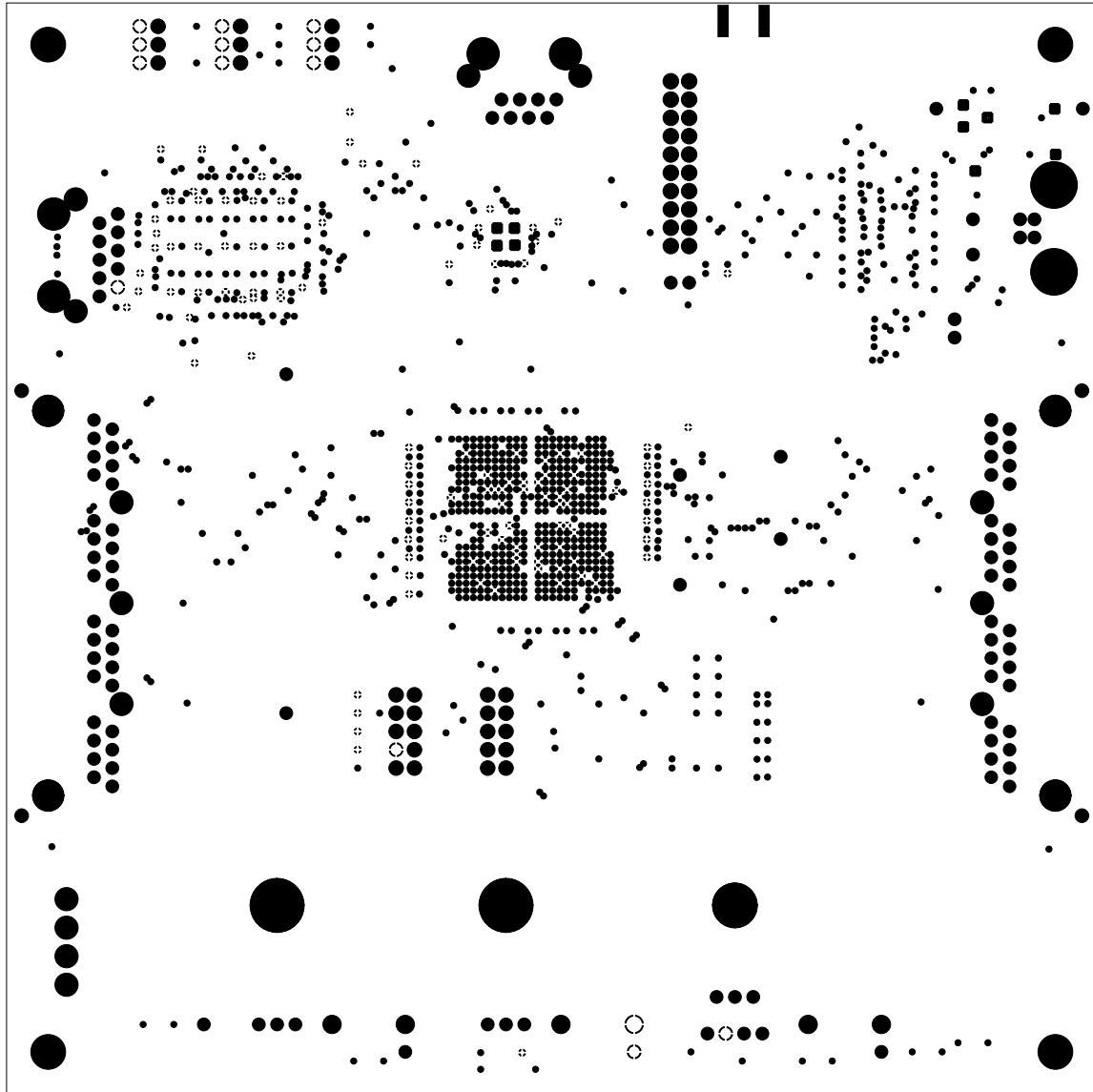
Layer1_Signal1_Top



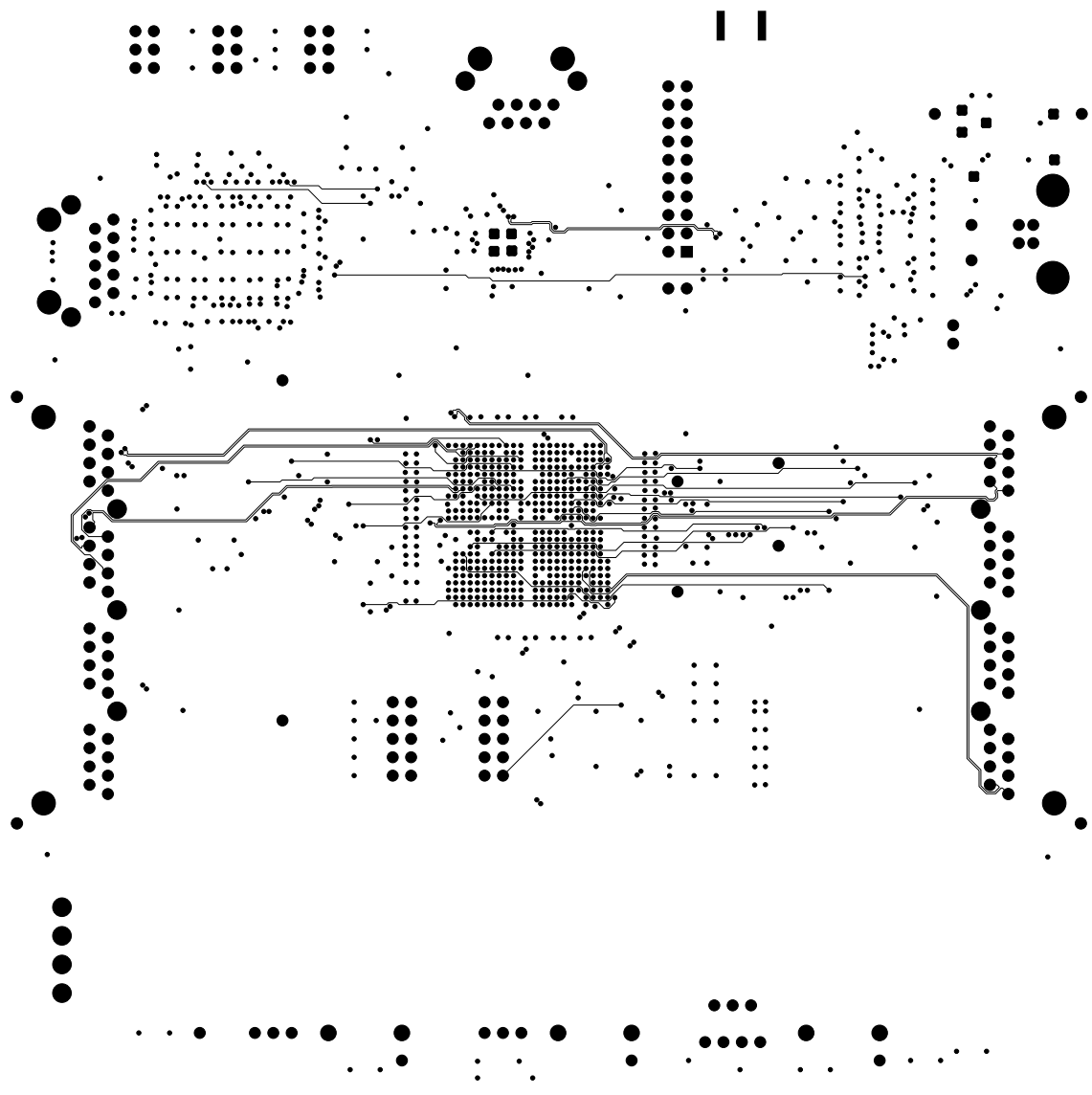
Layer2_Plane:ground



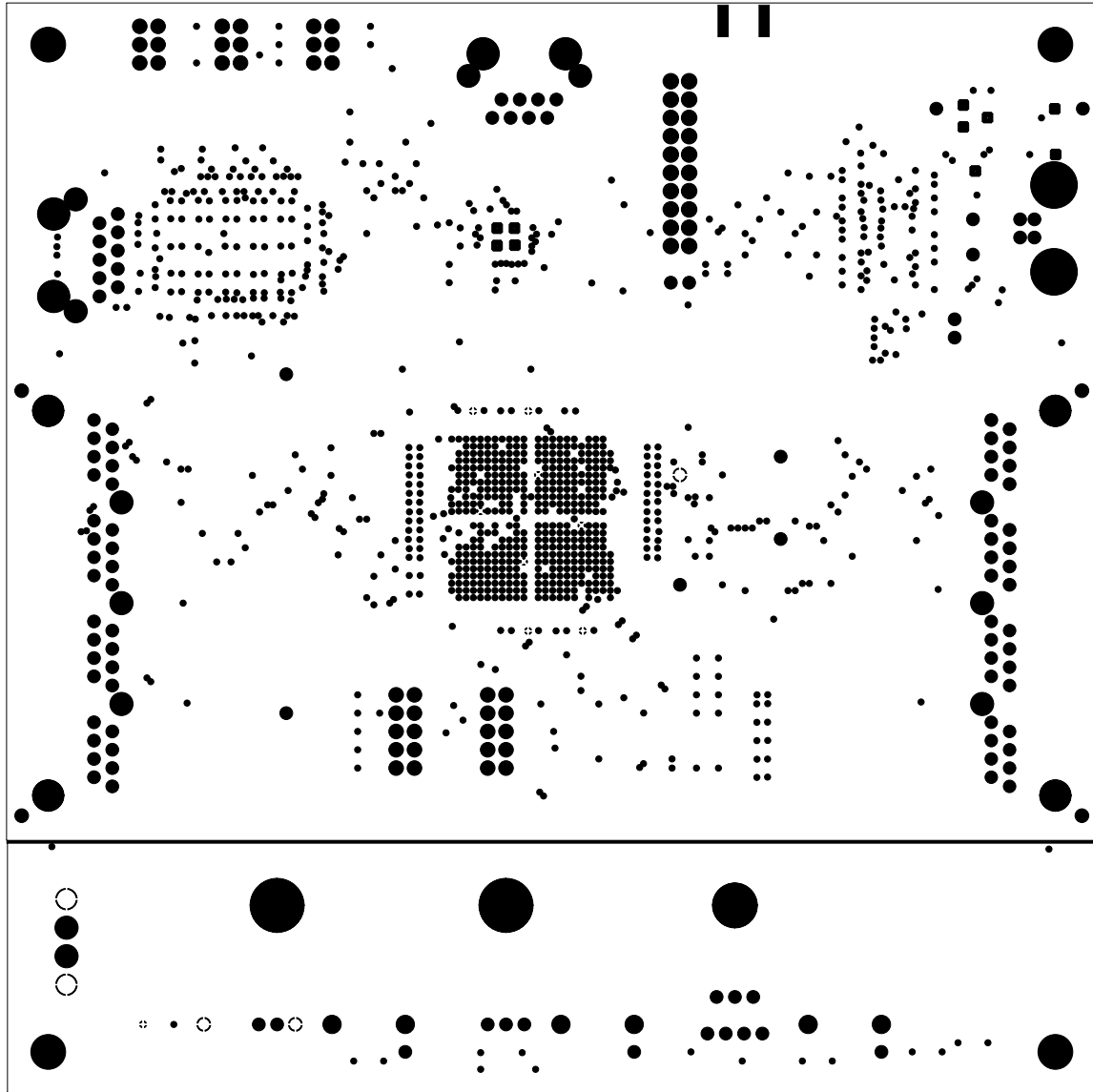
Layer3_Signal12



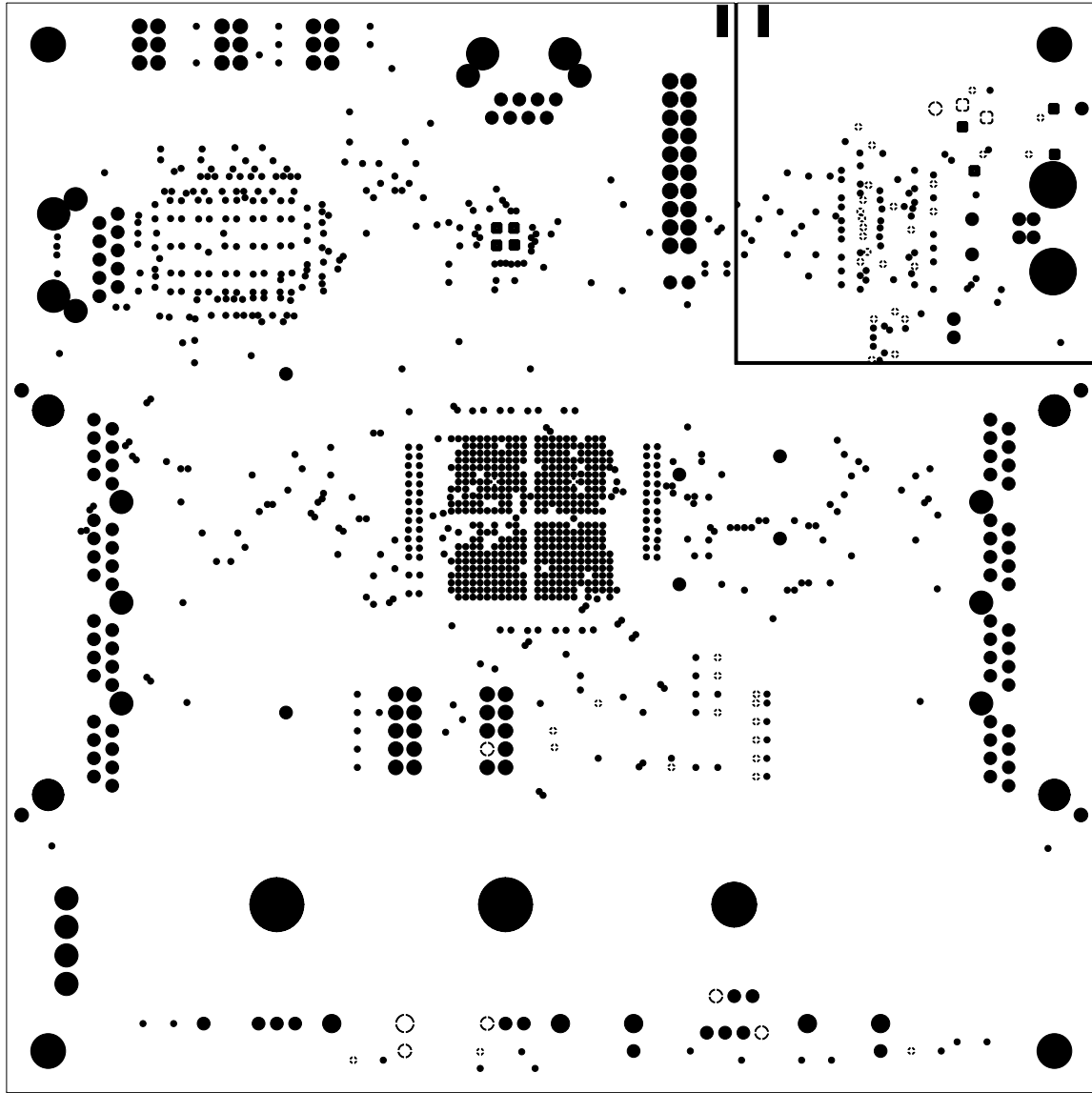
Layer4_Plane:+2.5V



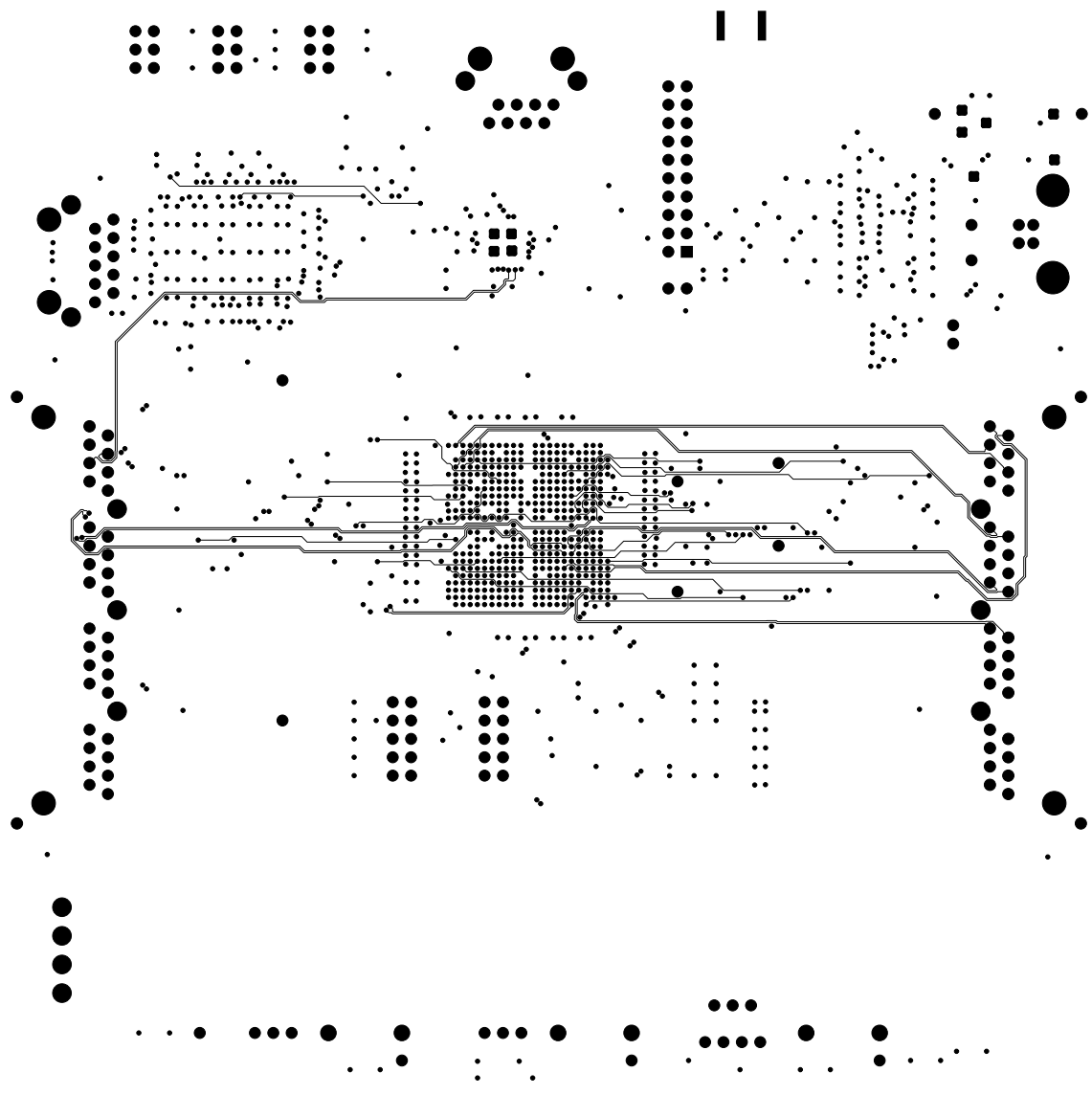
Layer5_Signal13



Layer6_Plane:+2.5V_PLL;+5V

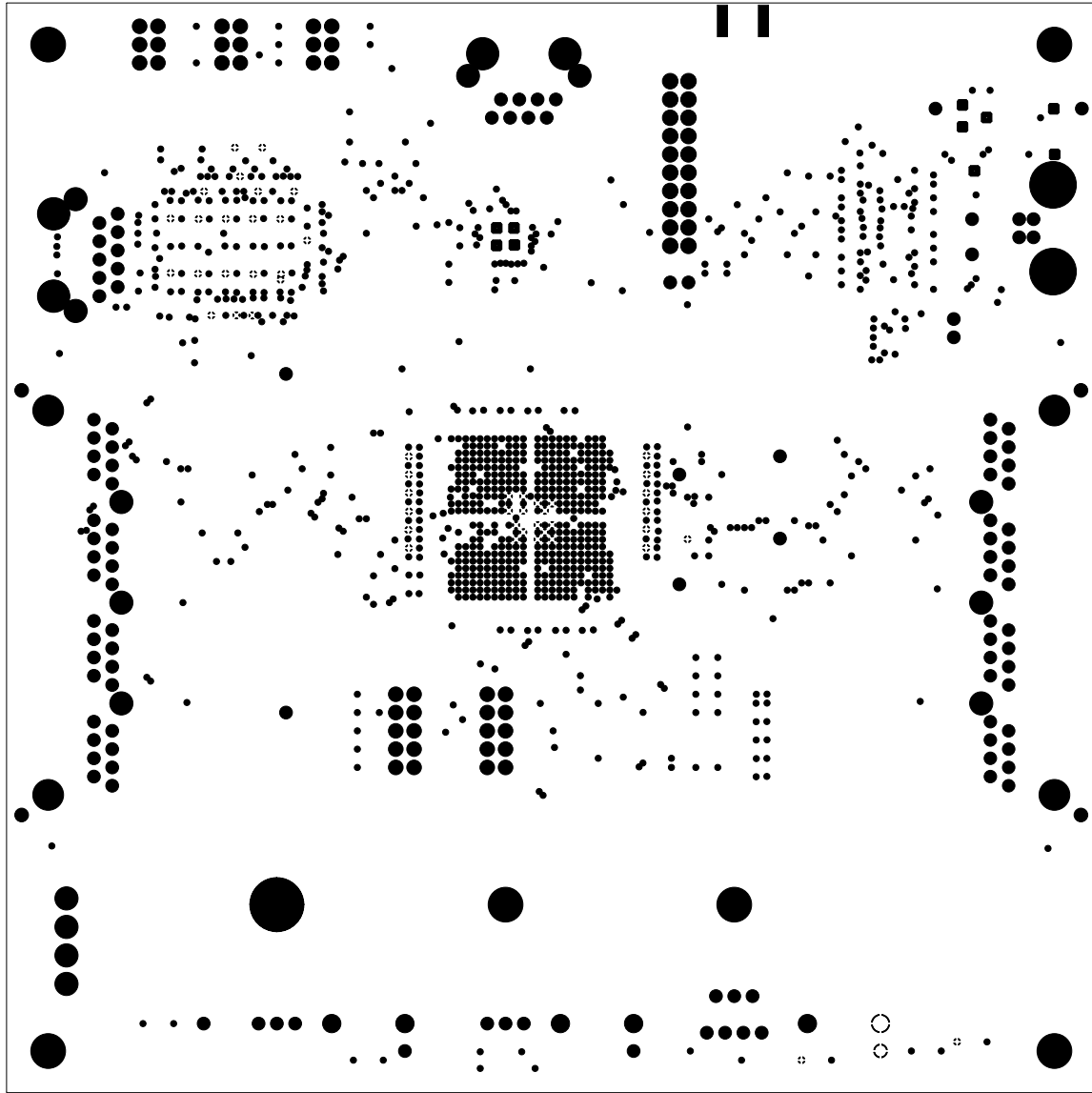


Layer7_Plane:+3.3V;+3.3V_USB

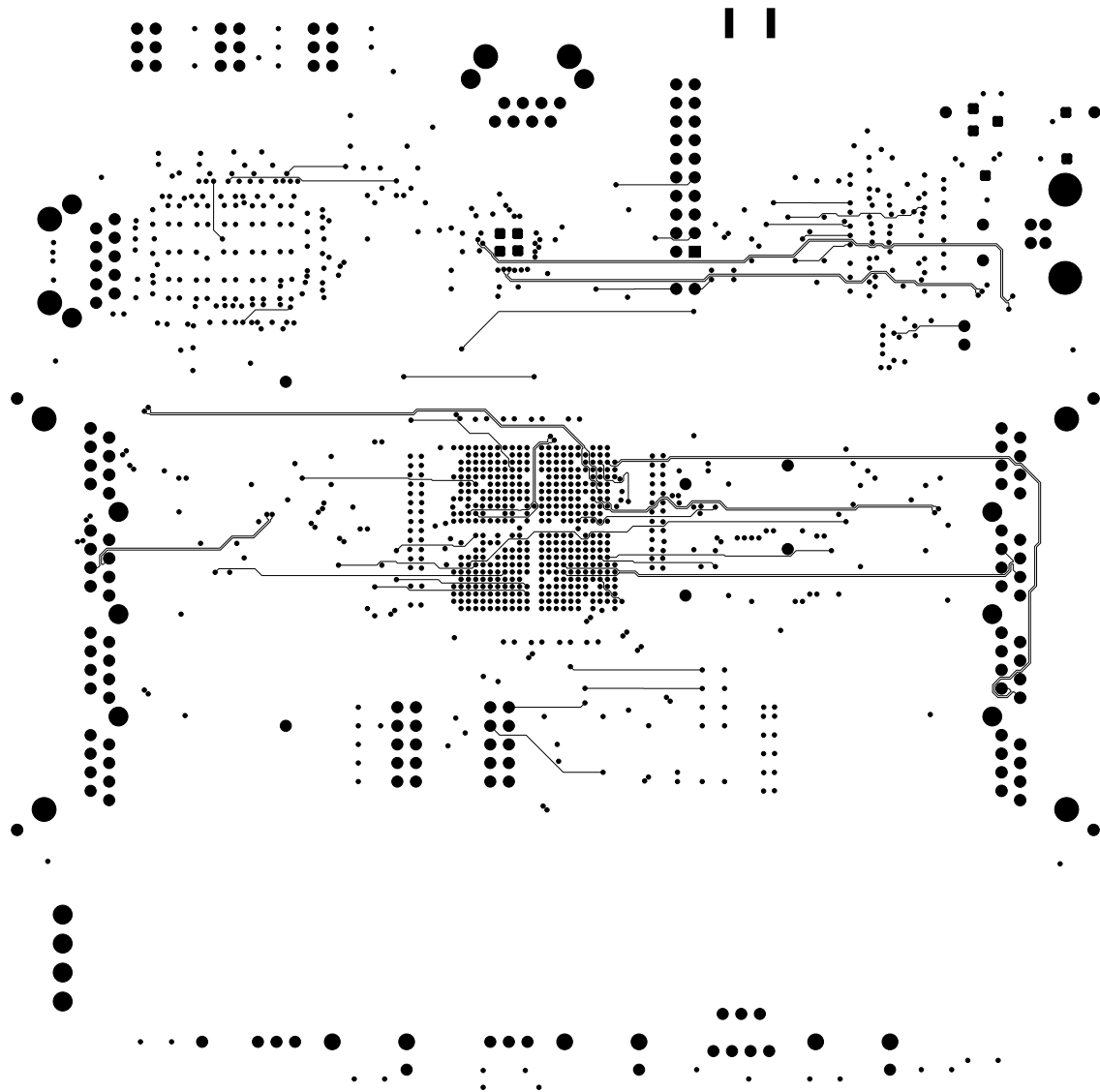


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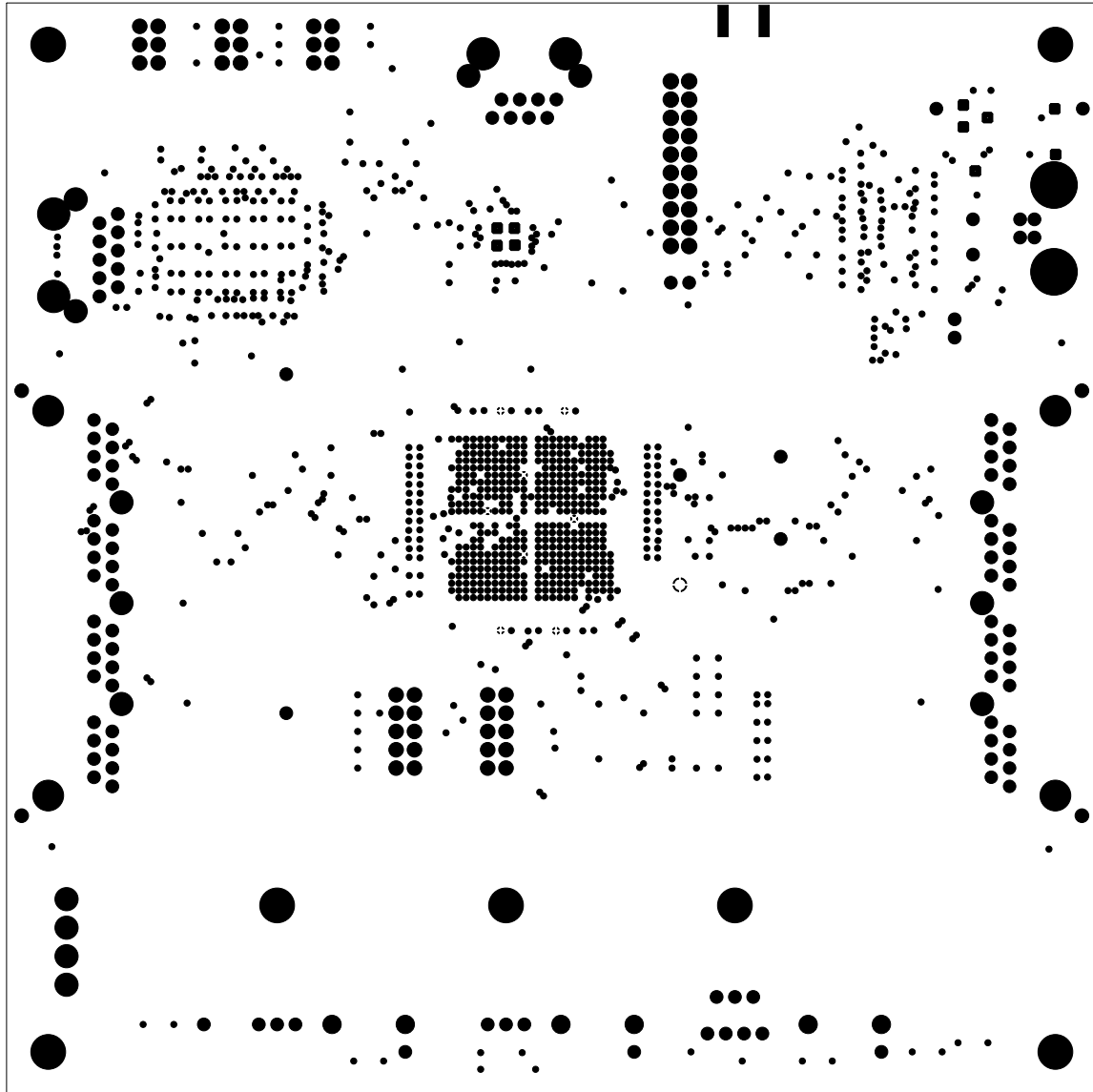
Layer8_Signal14



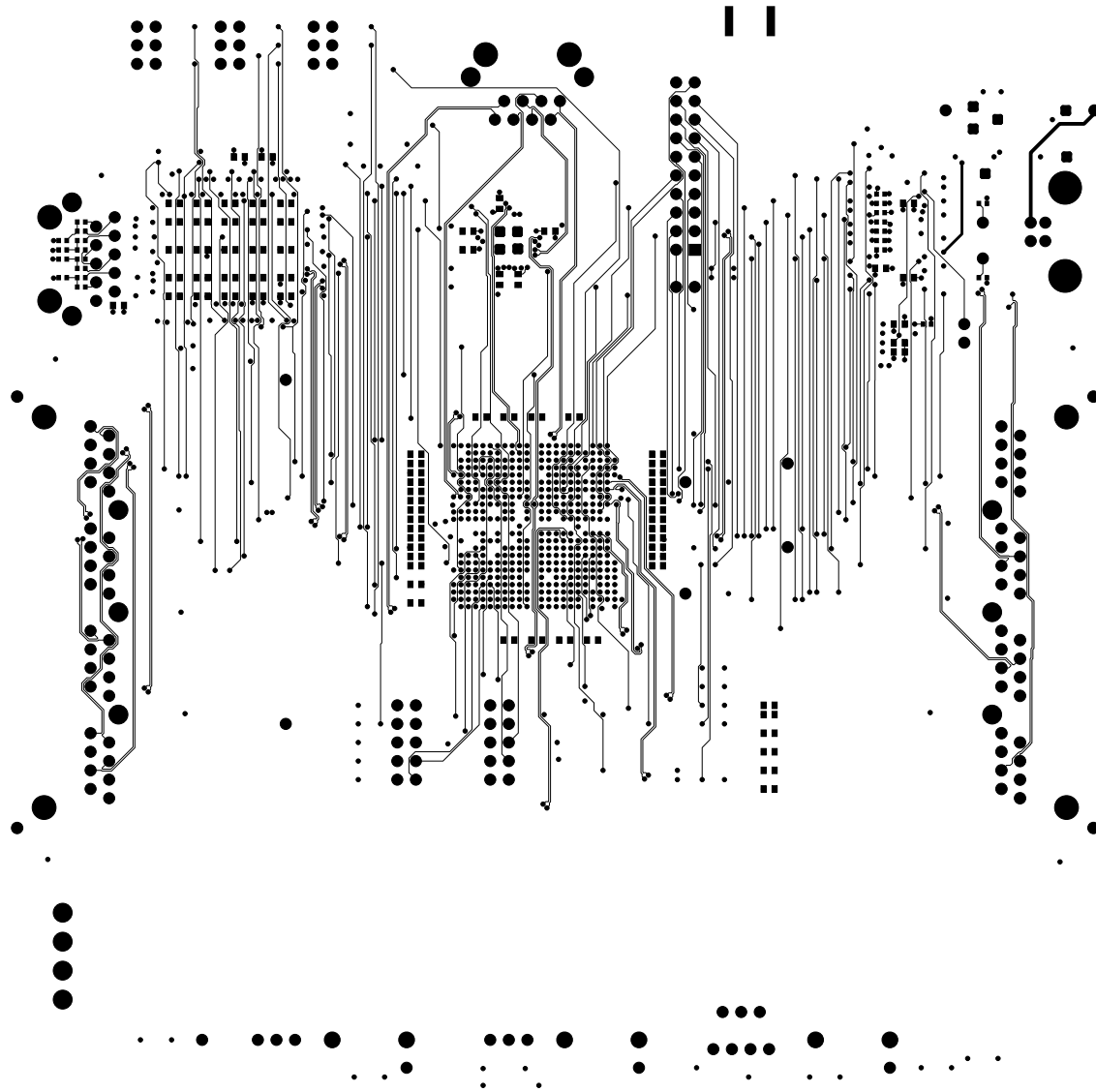
Layer9_Plane:+1.1V



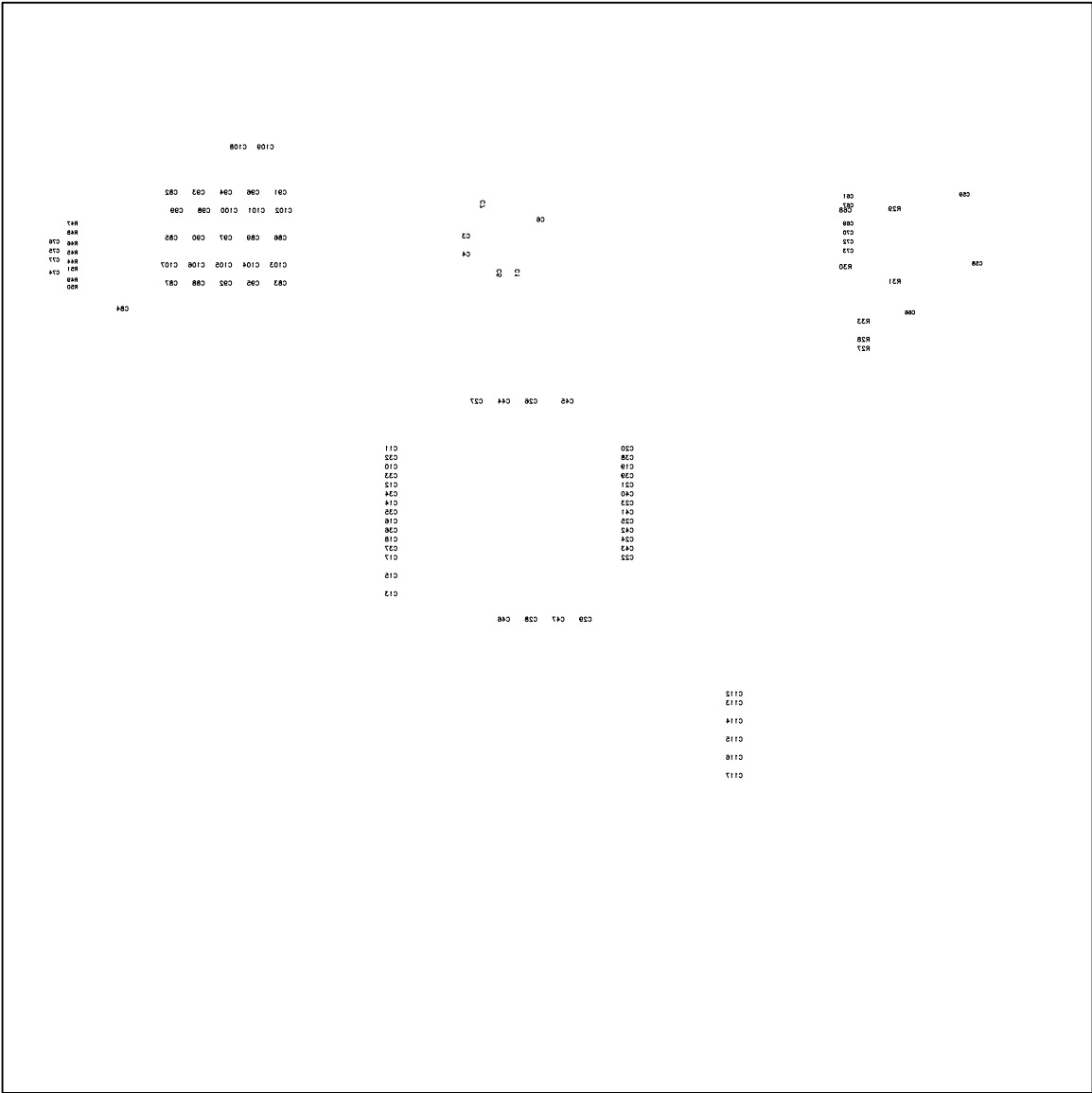
Layer10_Signal5



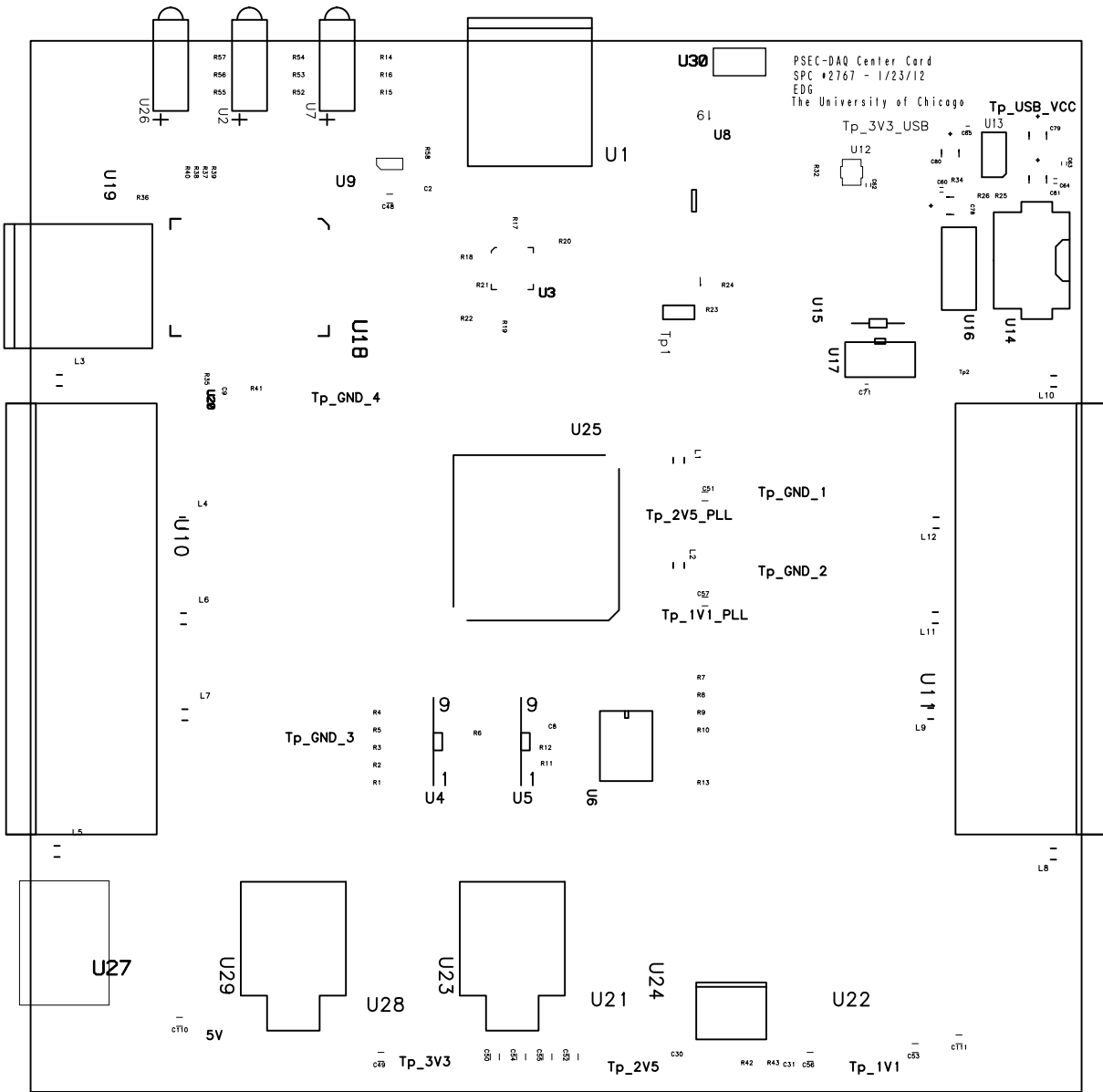
Layer11_Plane:+1.1V_PLL

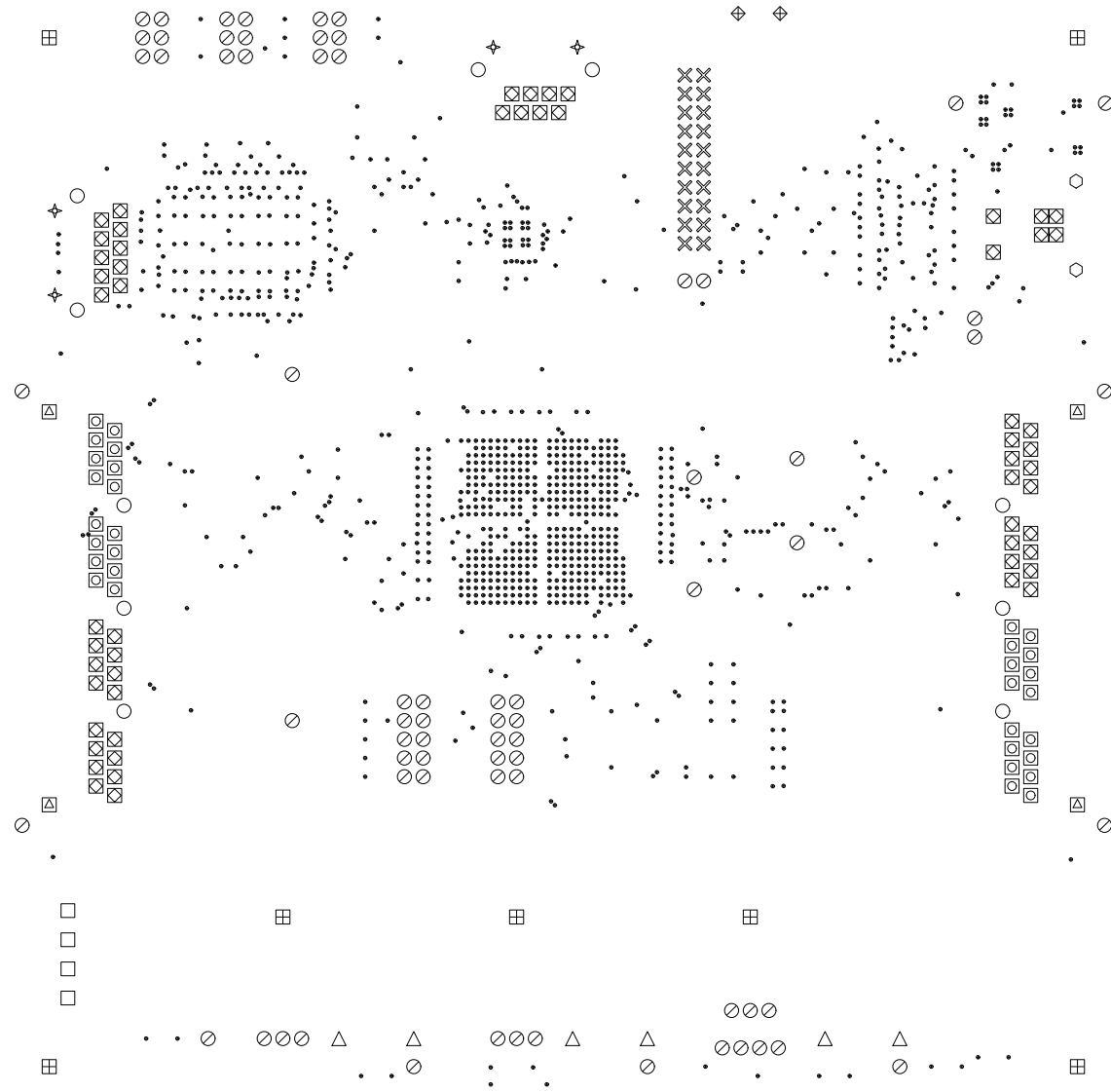


Layer12_Signal16_Bottom

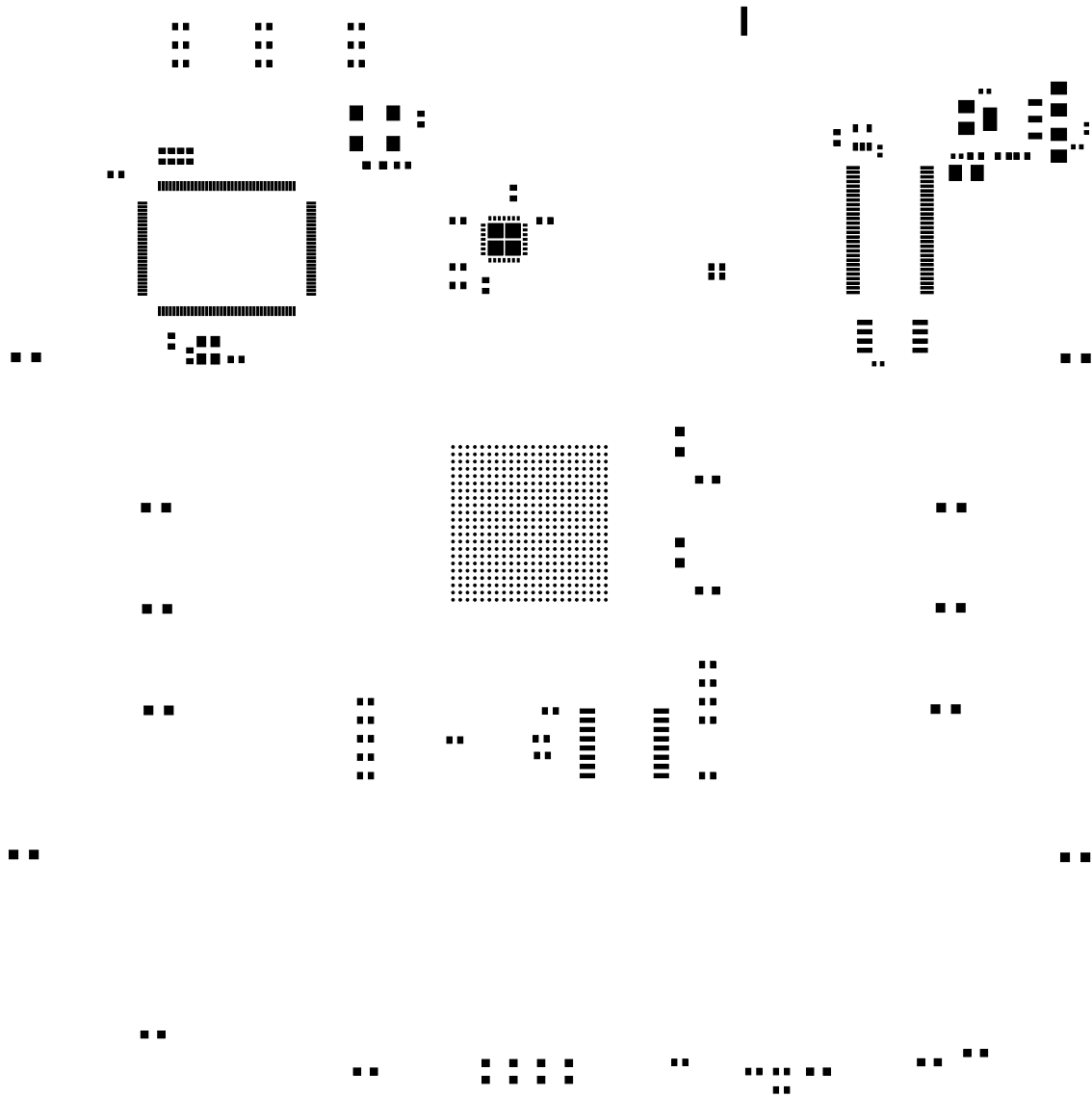


Layer16_Silk_Bottom

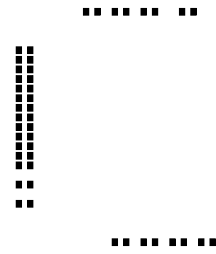
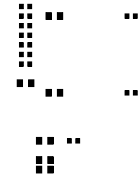
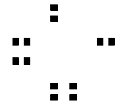
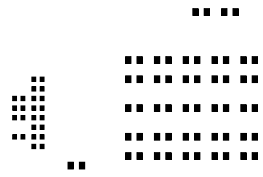




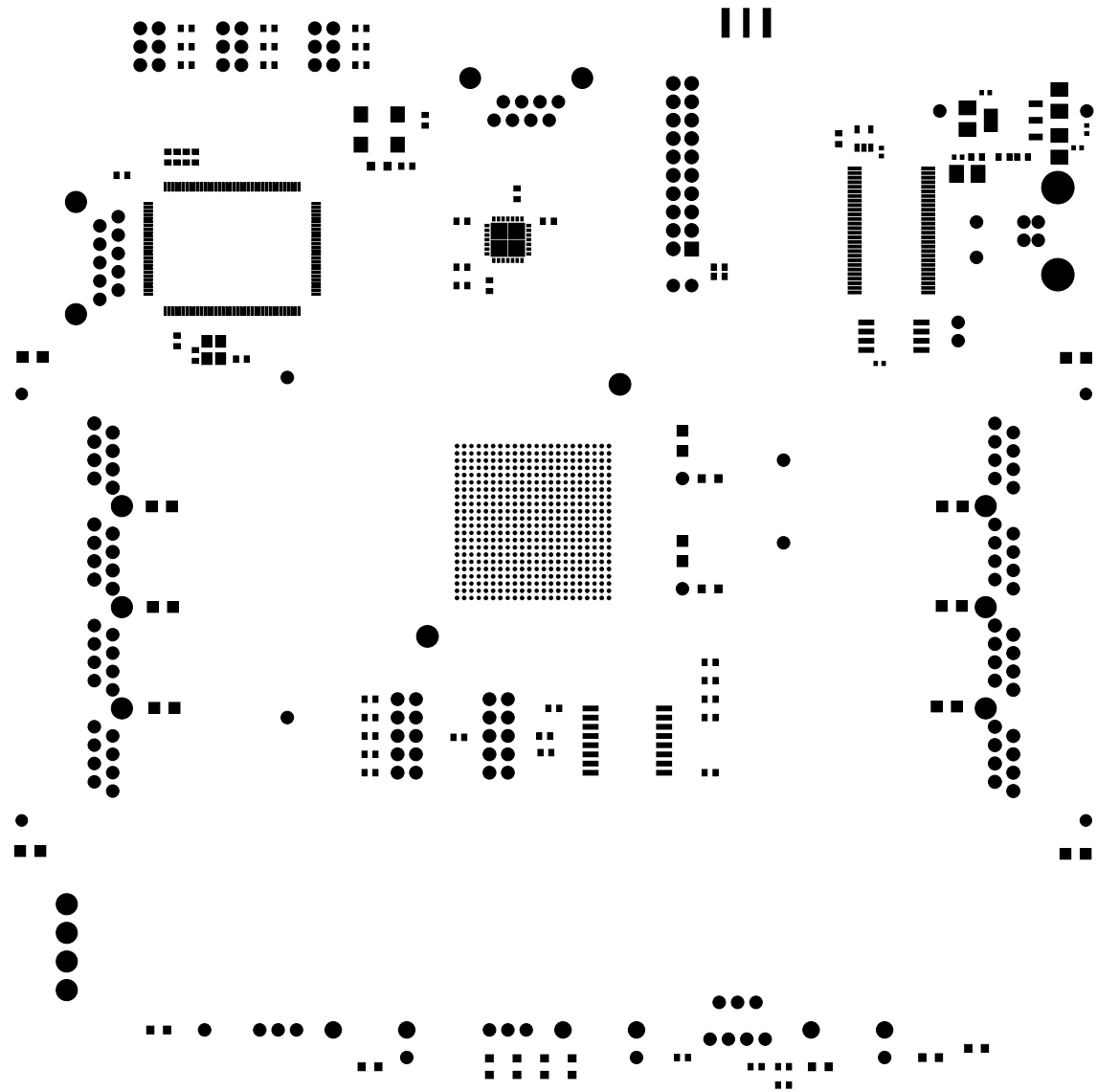
Through Holes						
Symbol	Diameter	Tolerance	Plated	Punched	HoleName	Quantity
⊕	0.0120	+0.0000 / -0.0030	Yes	No	Rnd 12 +Tol 0 -Tol -3	1086
⊕	0.0157		Yes	No	HOLE_40000.00000	2
⊗	0.0350		Yes	No	HOLE_88900.00000	56
⊗	0.0390		Yes	No	HOLE_99060.00000	32
○	0.0410		Yes	No	HOLE_104140.00000	71
⊗	0.0420		Yes	No	HOLE_106680.00000	20
Δ	0.0570		Yes	No	HOLE_144780.00000	6
○	0.0630		Yes	No	HOLE_160000.00000	10
□	0.0669		Yes	No	Rnd 1.7	4
○	0.0984		Yes	No	HOLE_250000.00000	2
⊗	0.1280		Yes	No	PLATEDHOLE_324993.000000	4
⊕	0.1280		Yes	No	PLATEDHOLE_325000.000000	4
⊗	0.1500		No	No	NONPLATEDHOLE_381000.000000	7



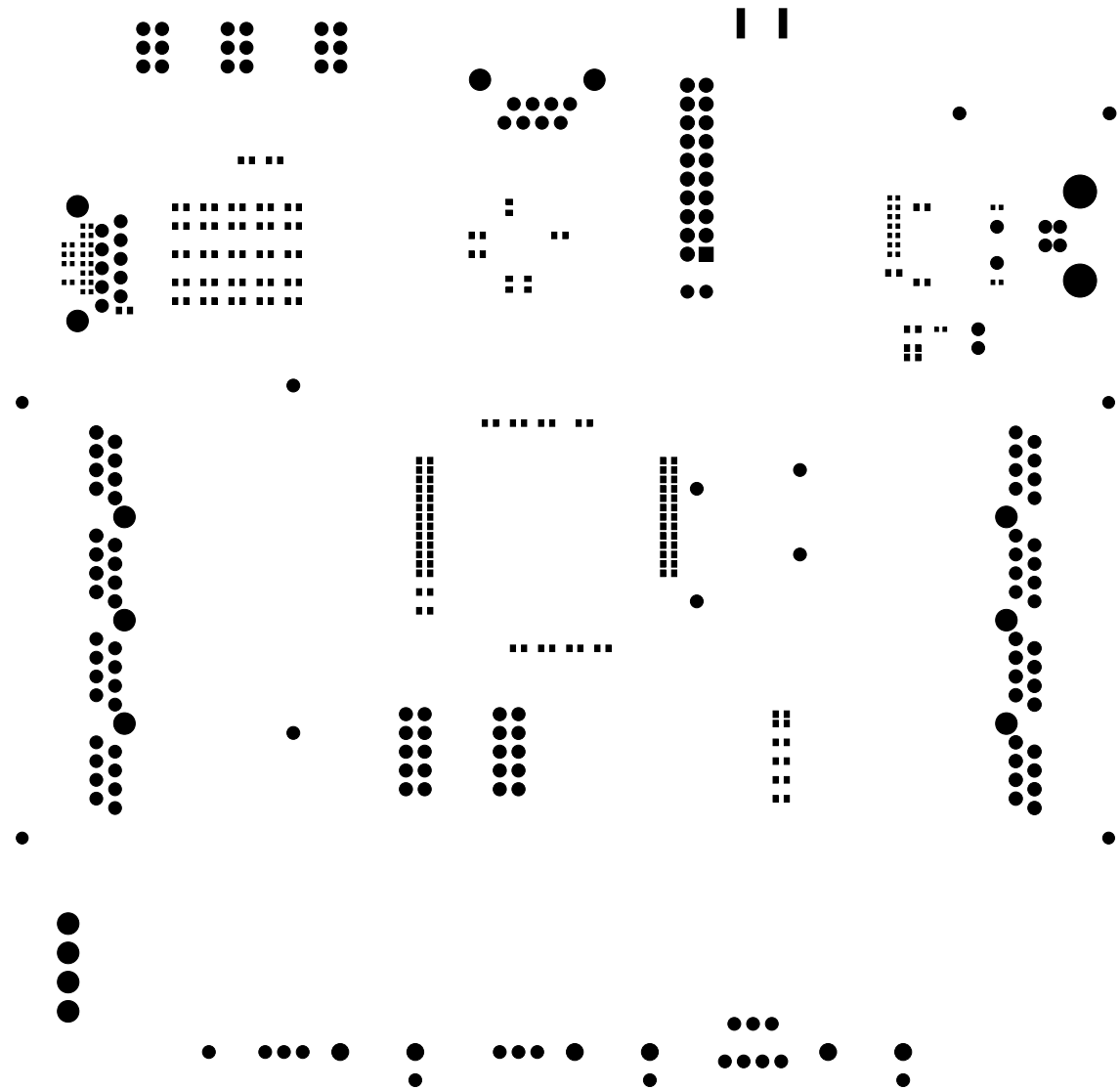
Layer17_Paste_Top



Layer18_Paste_Bottom



Layer13_Mask_Top



Layer14_Mask_Bottom