



| | | | |
|----------|-----------|----------------------------------------------------------------------|----------------------|
| Engineer | M. Bogdan | The University of Chicago 5640 S. Ellis Ave. Chicago, IL 60637 | |
| Drawn by | M. Bogdan | | |
| R&D CHK | | | |
| DATE: | 8/9/13 | TITLE | Size C |
| TIME: | 1:30 pm | LVDS Serializer Card JPARC-K 14BIT-ADC Board | |
| QA CHK | | REV A | DRW. 2820 Sheet 1 of |