1. All dimensions are given in inches unless specified otherwise.
2. Minimum trace width: 0.005" and clearance: 0.005"
3. 1 oz copper for all power layers and for Signal_1,2 (Top and Bottom)
4. Apply Solder Mask over bare copper.

- **Silkscreen on both sides**.
- **FMS tolerances: 4/0.005**
- **Interlayer spacing as specified**
- **Perform TDR test for all signal layers**
- **Present TDR test results for all signal layers**