



- Board Characteristics
- All dimensions are given in inches unless specified otherwise.
 - Minimum trace width: 0.005" and clearance: 0.005"
 - 1 oz copper for all power layers and for Signal_1,2 (Top and Bottom)
 - Apply Solder Mask over bare copper.
 - Silkscreen on both Sides.
 - FHS tolerances: +/- 0.003
 - Interlayer spacing as specified
 - Zc=50 Ohm +/- 5 Ohm
Perform TDR test for all signal layers.
Present TDR test results for all signal layers.

BOARD'S DRILL SCHEDULE

DRILL SYMBOL	DRILL SIZE	COUNT	PLATED	Tolerance	COMMENT
○	.0098425197	4	YES	---	
⊞	.014	25	YES	---	
⊕	.041	30	YES	---	
⊞	.098425197	2	NO	---	
⊖	.13779528	1	NO	---	

UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES TOLERANCES ARE: FRACTIONS DECIMALS ANGLES :XX :XXX DO NOT SCALE DRAWING			CONTRACT NO.		UNIVERSITY OF CHICAGO ELECTRONICS DEVELOPMENT GROUP		
TREATMENT			APPROVALS	DATE	TITLE KOTO Deserializer Specification Drawing		
FINISH			DRAWN M. Bogdan	8/9/13	SIZE B	FSCM NO.	DWG. NO. 2824
SIMILAR TO			CHECKED M. Bogdan	8/9/13	ISSUED	REV. A	
ACT. WT	CALC WT		SCALE 1/1		SHEET		