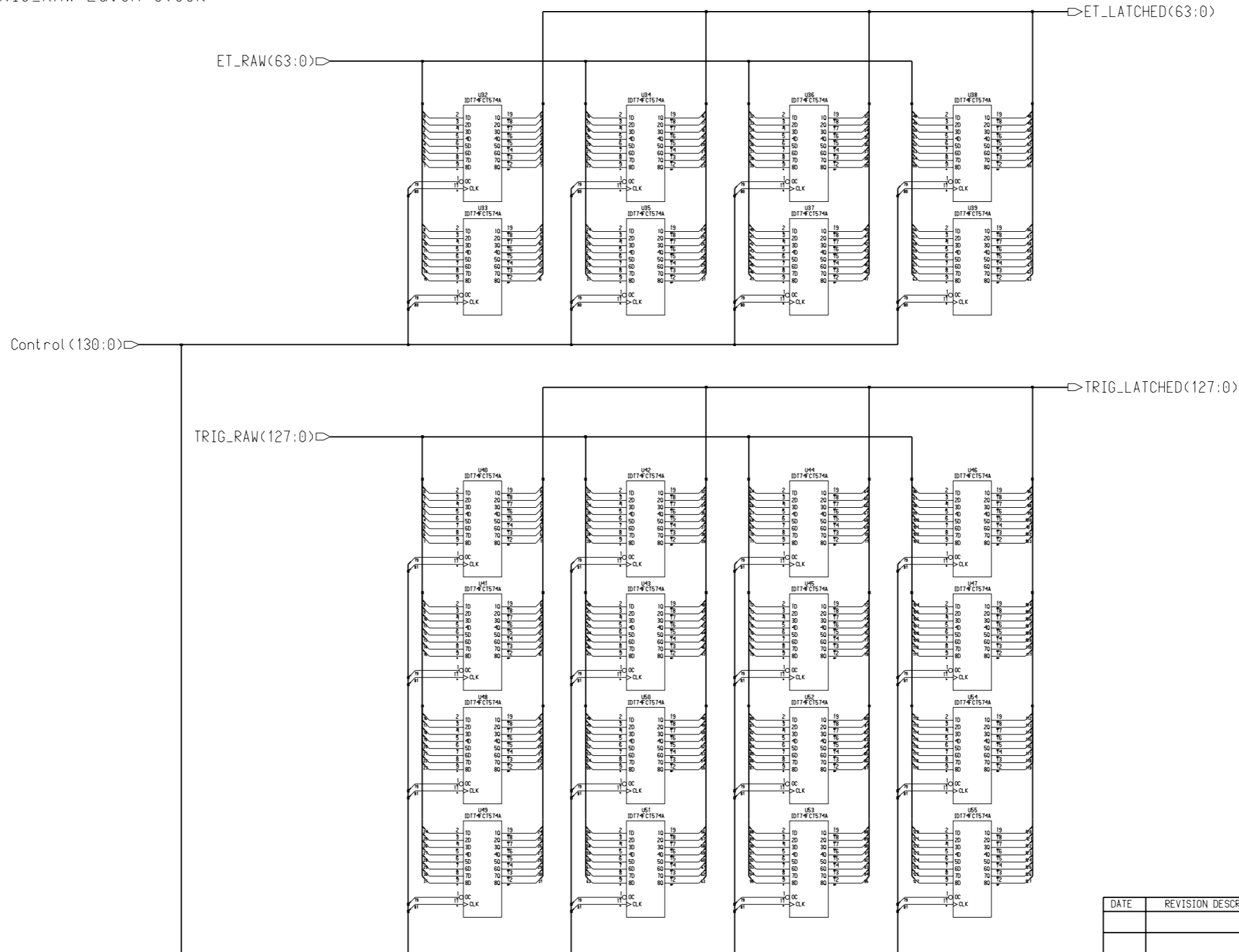


Control Signals

- Control(79) - Latch Output Enable *
- Control(80) - ET_RAW Latch Clock
- Control(81) - TRIG_RAW Latch Clock

P3_Latch



UNIVERSITY OF CHICAGO ELECTRONICS DEVELOPMENT GROUP	
TITLE Crate Sum/P3_Latch	
SHEET 11 OF 20	C-2329
DATE 9 July 98	REV A
DRWN Nancy J. Lei	

DATE	REVISION DESCRIPTION