Control Signals

Control(0) - Reset
Control(1) - Write Bunch Counter Clock
Control(2) - Read Clock
Control(30) - Bunch Counter FIFO Empty Flag
Control(31) - Bunch Counter FIFO Full Flag
Control(32) - Read Address B (Set 1)
Control(33) - Read Address A (Set 1)
Control(34) - Write Address B (Set 1)
Control(35) - Write Address A (Set 1)
Control(36) - Buffer Write Enable (1)
Control(37) - Bunch Counter Buffer Read Enable
Control(38) - Bunch Counter Bit 0
Control(39) - Bunch Counter Bit 1
Control(40) - Bunch Counter Bit 2
Control(41) - Bunch Counter Bit 3
Control(42) - Bunch Counter Bit 4
Control(43) - Bunch Counter Bit 5
Control(44) - Bunch Counter Bit 6
Control(45) - Bunch Counter Bit 7

UNIVERSITY OF CHICAGO
ELECTRONICS DEVELOPMENT GROUP

TITLE
Bunch Counter

DATE
21 Aug 20
DRAWN
V1.0

C-2329