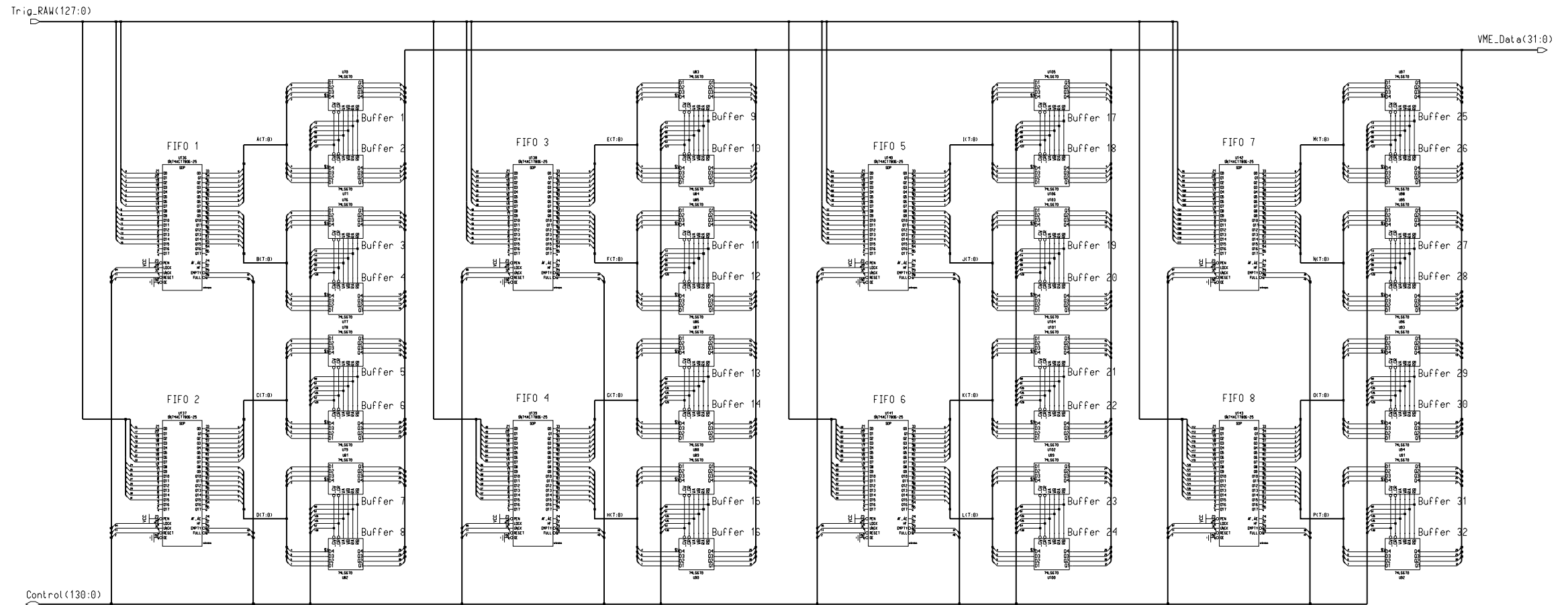


Trig_RAW_DAO

Control Signals

- | | | | |
|-------------|------------------------------|--------------|--|
| Control(2) | - Reset* | Control(13) | - Read Address B (Set 1) |
| Control(11) | - Read Clock | Control(14) | - Read Address A (Set 1) |
| Control(32) | - Trig_RAW FIFO1 Empty flag* | Control(50) | - Read Address B (Set 2) |
| Control(33) | - Trig_RAW FIFO2 Empty flag* | Control(51) | - Read Address A (Set 2) |
| Control(34) | - Trig_RAW FIFO3 Empty flag* | Control(88) | - Write Address B (Set 1) |
| Control(35) | - Trig_RAW FIFO4 Empty flag* | Control(89) | - Write Address A (Set 1) |
| Control(36) | - Trig_RAW FIFO5 Empty flag* | Control(125) | - Write Address B (Set 2) |
| Control(37) | - Trig_RAW FIFO6 Empty flag* | Control(126) | - Write Address A (Set 2) |
| Control(38) | - Trig_RAW FIFO7 Empty flag* | Control(127) | - Buffer Write Enable* (Driver output 1) |
| Control(39) | - Trig_RAW FIFO8 Empty flag* | Control(128) | - Buffer Write Enable* (Driver output 2) |
| Control(40) | - Trig_RAW FIFO1 Full flag* | Control(129) | - Buffer Write Enable* (Driver output 3) |
| Control(41) | - Trig_RAW FIFO2 Full flag* | Control(130) | - Buffer Write Enable* (Driver output 4) |
| Control(42) | - Trig_RAW FIFO3 Full flag* | Control(62) | - Trig_RAW Buffers 1-8 Read Enable* |
| Control(43) | - Trig_RAW FIFO4 Full flag* | Control(63) | - Trig_RAW Buffers 9-16 Read Enable* |
| Control(44) | - Trig_RAW FIFO5 Full flag* | Control(64) | - Trig_RAW Buffers 17-24 Read Enable* |
| Control(45) | - Trig_RAW FIFO6 Full flag* | Control(65) | - Trig_RAW Buffers 25-32 Read Enable* |
| Control(46) | - Trig_RAW FIFO7 Full flag* | Control(84) | - Write Trig_RAW Quarter 1 Clock |
| Control(47) | - Trig_RAW FIFO8 Full flag* | Control(85) | - Write Trig_RAW Quarter 2 Clock |
| | | Control(86) | - Write Trig_RAW Quarter 3 Clock |
| | | Control(87) | - Write Trig_RAW Quarter 4 Clock |



UNIVERSITY OF CHICAGO
ELECTRONICS DEVELOPMENT GROUP

TITLE
Crate Sum/Trig_RAW_DAO

DATE	REVISION DESCRIPTION	SHEET	OF	DATE	DRWN
1 Oct 98	Changed to 11 SMTACT1885 FIFO chip - NA	18	20	3 July 98	Nancy J. Lai
29 Oct 98	Added read addresses, write addresses, and write enables changed to driver outputs. NA				

C-2329
REV A