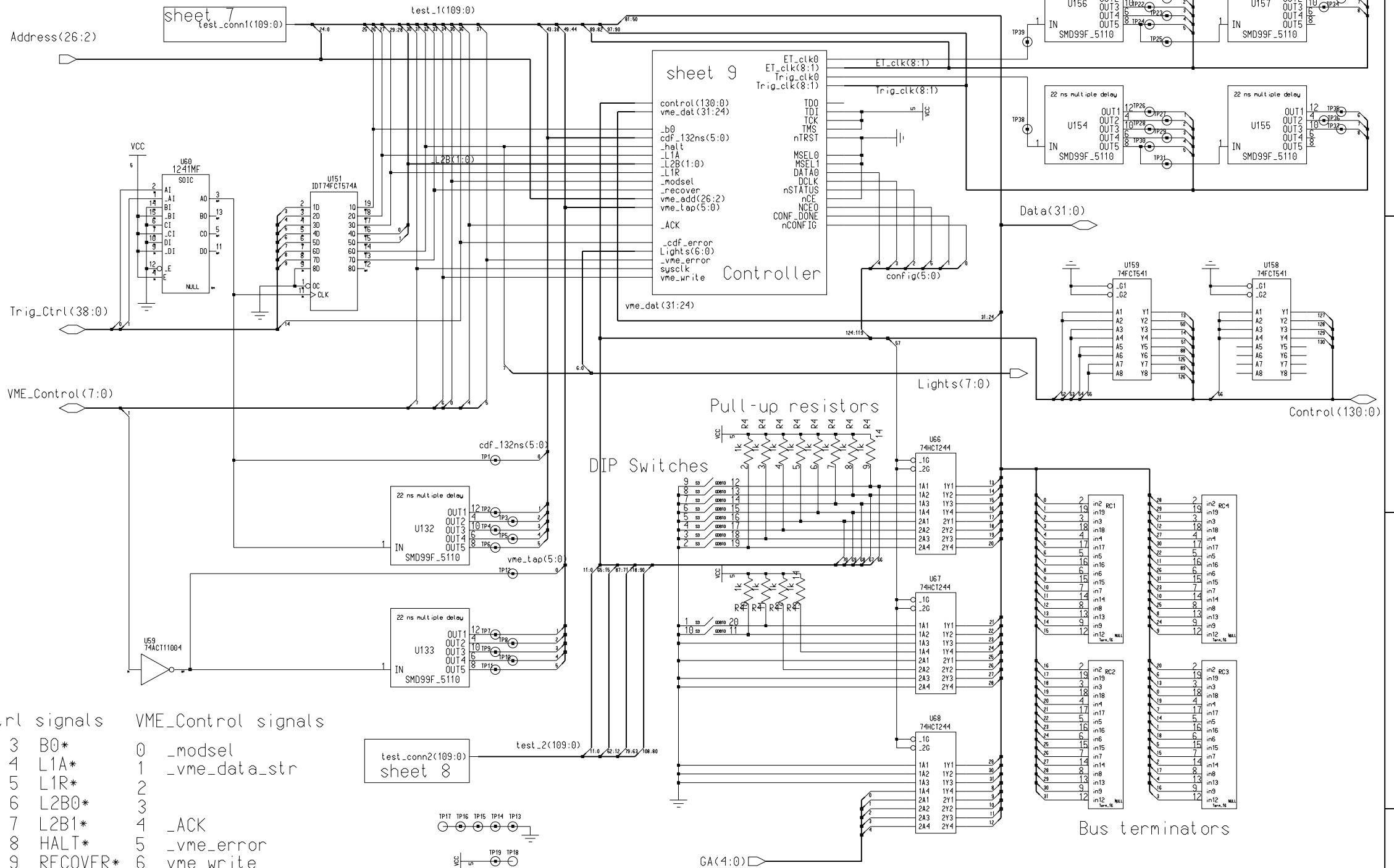


Configuration Device 1: Controller



- Trig_Ctrl signals
- 3 B0*
 - 4 L1A*
 - 5 L1R*
 - 6 L2B0*
 - 7 L2B1*
 - 8 HALT*
 - 9 RECOVER*
 - 14 ERROR*
- VME_Control signals
- 0 _modsel
 - 1 _vme_data_str
 - 2
 - 3
 - 4 _ACK
 - 5 _vme_error
 - 6 vme_write
 - 7 sysclk

- config signals
- 5 NCEO
 - 4 DATA0
 - 3 DCLK
 - 2 nSTATUS
 - 1 CONF_DONE
 - 0 nCONFIG
- Data bits 13 - 22 are the Board Serial Number bits

UNIVERSITY OF CHICAGO
ELECTRONICS DEVELOPMENT GROUP

TITLE
CrateSum / Control

DATE	REVISION DESCRIPTION
18 Oct 98	Added delay chips (4) for ET and Trig timing. --NJL
29 Oct 98	Added two 74FCT541 line drivers. --NJL

SHEET 6 OF 20 DATE 26 Aug 98 DRWN Nancy J. Lai	C-2329 REV A
--	-----------------