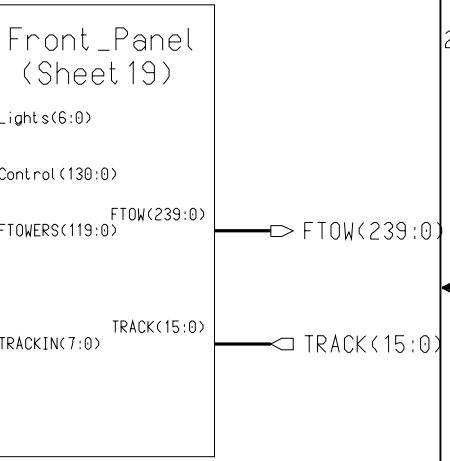


- ISP Programming Chain
- 0) EtInp Chip 0
  - 1) EtInp Chip 1
  - 2) EtInp Chip 2
  - 3) Csum Mux Chip
  - 4) TowMask Tower 0
  - 5) TowMask Tower 1
  - 6) TowMask Tower 2
  - 7) TowMask Tower 3
  - 8) TowMask Tower 4
  - 9) TowMask Tower 5
  - 10) The Brain
  - 11) Error Chip
  - 12) Bunch Counter
  - 13) The Heart
  - 14) VME Interface



UNIVERSITY OF CHICAGO  
ELECTRONICS DEVELOPMENT GROUP

TITLE  
**DIRAC**

| DATE          | REVISION DESCRIPTION                     |
|---------------|--|
| 28 June 1995  | Second Prototype: Major Changes all over |
| 22 April 1998 | Third Prototype: Bug Fixes               |
|               |  |

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| SHEET 1 OF 24    | C-2351 |
| DATE 18 Nov 1996 | REV C  |
| DRWN JW          |        |

| DATE          | REVISION DESCRIPTION                     |
|---------------|--|
| 28 June 1995  | Second Prototype: Major Changes all over |
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|               |  |