



Trig_Ctrl are signals sent along the user defined pins of P2 for Global signals sent throughout the trigger. NOTE: Be sure to buffer any P2 signals going to more than one chip on the board, so that the tracer doesn't get overloaded.

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TITLE
DIRAC / VME Interface

DATE	REVISION DESCRIPTION
16 Dec 93	Original design -- YF
18 Nov 96	3rd Prototype - Bug Fixes -- JW
10 Oct 97	Replace VME2000 & other with single PLD

SHEET 2 OF 24
DATE 10 Oct 1997
DRWN AA / MB / JW
C-2351
REV C