



Rows Z and D are in accordance with the VME64 Extensions standard. Note: the positioning of rows A, B and C should be in accordance with the VME Standard.

- All user-defined signals on P2 are totem-pole TTL excluding:
- a. Geographical Addresses are pulled-up/grounded wires
- b. 132ns Clock is differential PECL: CLK - CLK\*
- c. ERROR\* is open collector TTL

UNIVERSITY OF CHICAGO ELECTRONICS DEVELOPMENT GROUP	
TITLE GhostBuster / VMEbus_P2	
DATE	REVISION DESCRIPTION
1989-12-11	Started from Prefixed P2 Rev C - ... NAGAWA
SHEET	DATE
OF	18 Nov 1986
DATE	REV
18 Nov 1986	A