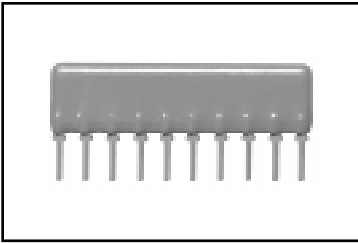




MODEL CS206 Resistor/Capacitor Networks

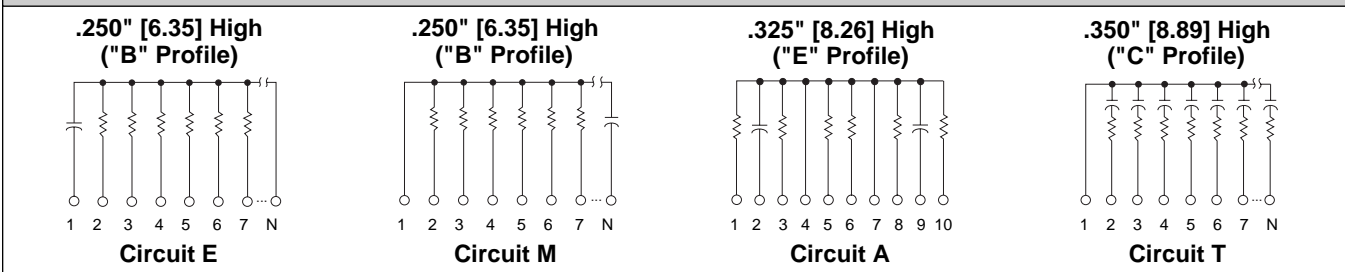
ECL Terminators and Line Terminator
Conformal Coated, SIP



FEATURES

- 4 to 18 pins available
- X7R and COG capacitors available
- Low cross talk
- Custom design capability
- "B" .250" [6.35mm], "C" .350" [8.89mm] and "E" .325" [8.26mm] maximum seated height available, dependent on schematic
- 10k ECL terminators, Circuits E and M. 100k ECL terminators, Circuit A. Line terminator, Circuit T.

SCHEMATICS [Numbers in brackets indicate millimeters]



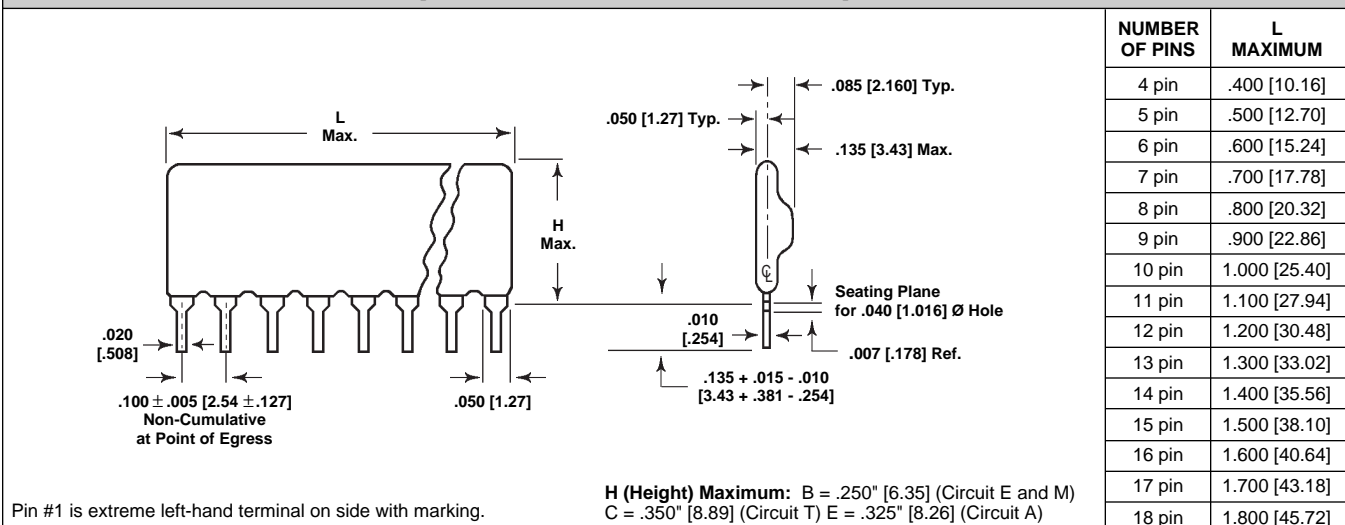
ELECTRICAL SPECIFICATIONS - CAPACITORS

- Capacitance:** 0.01 μ F for Circuits E, M and A.
33pF to 0.1 μ F for Circuit T.
- Capacitance Tolerance:** $\pm 10\%$ (K), $\pm 20\%$ (M).
- EIA Characteristics:** COG and X7R. (COG capacitors may be substituted for X7R capacitors.)
- Dissipation Factor:** COG maximum .15%.
X7R maximum 2.5%.
- Temperature Coefficient:** COG ± 30 PPM/ $^{\circ}$ C.
X7R $\pm 15\%$.
- Dielectric Test:** 2.5 x rated voltage.
- Moisture Resistance:** Meets requirements of MIL-STD-202, Method 106.
- Operating Voltage:** 50 volts at + 125 $^{\circ}$ C.
- Insulation Resistance:** 1,000 ohm-farads or 100,000 Megohm, whichever is less at + 25 $^{\circ}$ C at rated voltage.

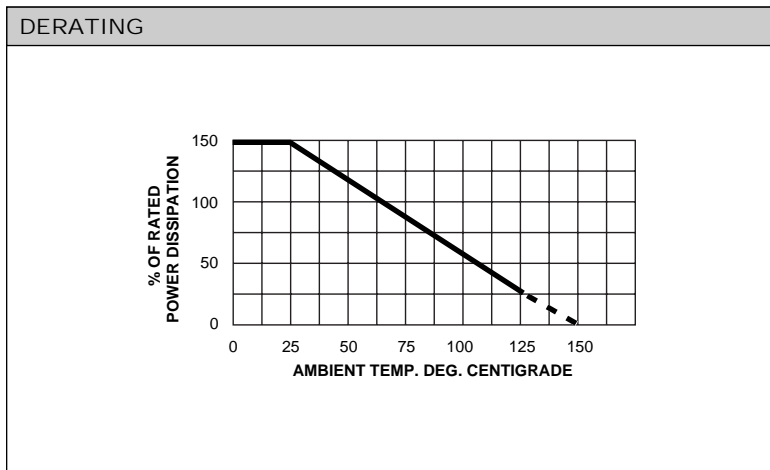
ELECTRICAL SPECIFICATIONS - RESISTORS

- Resistance Range:** 10 ohm to 1 Megohm.
- Resistance Tolerance:** $\pm 2\%$ and $\pm 5\%$.
- Temperature Coefficient:** ± 200 PPM/ $^{\circ}$ C.
- TCR Tracking:** ± 100 PPM/ $^{\circ}$ C.
- Operating Temperature Range:** - 55 $^{\circ}$ C to + 125 $^{\circ}$ C.
- Operating Voltage:** 50 volt maximum.
- Package Power Rating:** 8 pins: .8 watt maximum.
9 pins: .9 watt maximum. 10 pins: 1.0 watt maximum at + 70 $^{\circ}$ C.
- Power Per Resistor:** 125mW maximum at + 70 $^{\circ}$ C.
- MATERIAL SPECIFICATIONS**
- Flammability:** UL 94V-0.
- Lead Material:** Phosphorus-bronze, tin plated.
- Body Material:** Epoxy coated.
- Solderability:** Per MIL-STD-202, Method 208E.

DIMENSIONAL CONFIGURATIONS [Numbers in brackets indicate millimeters]



ENVIRONMENTAL PERFORMANCE		
TEST	CONDITION	MAX. ΔR (Typical Test Lots)
Thermal Shock	Subject to 5 cycles from - 65°C to + 125°C.	± 0.5% ΔR
Short Time Overload	2 1/2 x rated working voltage for 5 seconds at + 25°C.	± 0.25% ΔR
Moisture Resistance	Cycle from + 25°C to + 65°C to + 25°C over 8 hours at 90 - 98% relative humidity, with 10% of rated power applied, for 20 cycles. Stop cycling after an even number of cycles and stabilize networks at high humidity for 1 to 4 hours. Condition networks at - 10°C for 3 hours, then return to temperature cycling. On completion of cycling condition networks at + 25°C at 50% r.h. for 22 to 24 hours.	± 0.5% ΔR
Resistance to Soldering Heat	Immerse pins in melted solder to the lead standoffs at + 350°C for 3 seconds maximum.	± 0.25% ΔR
Mechanical Shock	18 shocks of 100 G and 6 ms.	± 0.25% ΔR
Vibration	12 cycles varied logarithmically from 10Hz to 2000Hz to 10Hz over 20 minutes.	± 0.25% ΔR
Load Life	1000 hours at + 70°C, rated power applied 1 1/2 hours on, 1/2 hour off.	± 1.0% ΔR
Resistance to Solvents	Immerse and scrub samples with isopropyl alcohol, trichlorethylene and Freon TMC.	Marking remains legible
Solderability	Immerse leads in 60/40 tin-lead solder using R flux at + 245°C for 5 seconds maximum.	Minimum 95% solder coverage
Terminal Strength	Withstand 2.2 kg pull 1 minute.	± 0.25% ΔR
Case Insulation Resistance	100 V applied between case and terminals tied together.	IR = 10,000 Megohm minimum



PART MARKING

- Pin #1 identification
- Part number
(Abbreviated as space allows)
- DALE® or D
- Date code

