UNIVERSITY OF CHICAGO
Electronics Design Group

TITLE: Level2 Fanout FRED Input 3

Size: B

Design: MB/TL
Check:
01/22/04

REV: A
Drawing Number: 2541
Sheet 6 of 17

Note: FP1 is a 80-pin Honda RPS connector

Input connector (from FRED) IFin(79:0)

FPGA_In(79:0)

SW(11:33:0)

VCC

GND

DS90LV018A

DS90LV018A

DS90LV018A

DS90LV018A

DS90LV018A

DS90LV018A

DS90LV018A

DS90LV018A

DS90LV018A

DS90LV018A

LVDS_Out1-

LVDS_Out1+

LVDS_Out2-

LVDS_Out2+

LVDS_Out3-

LVDS_Out3+

LVDS_Out4-

LVDS_Out4+

LVTTL_In-

LVTTL_In+

IGND

LVTTL_In-0
LVTTL_In-1
LVTTL_In-2
LVTTL_In-3
LVTTL_In-4
LVTTL_In-5
LVTTL_In-6
LVTTL_In-7
LVTTL_In-8
LVTTL_In-9
LVTTL_In-10
LVTTL_In-11
LVTTL_In-12
LVTTL_In-13
LVTTL_In-14
LVTTL_In-15
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LVTTL_In-26
LVTTL_In-27
LVTTL_In-28
LVTTL_In-29
LVTTL_In-30
LVTTL_In-31

LVTTL_In+0
LVTTL_In+1
LVTTL_In+2
LVTTL_In+3
LVTTL_In+4
LVTTL_In+5
LVTTL_In+6
LVTTL_In+7
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LVTTL_In+11
LVTTL_In+12
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