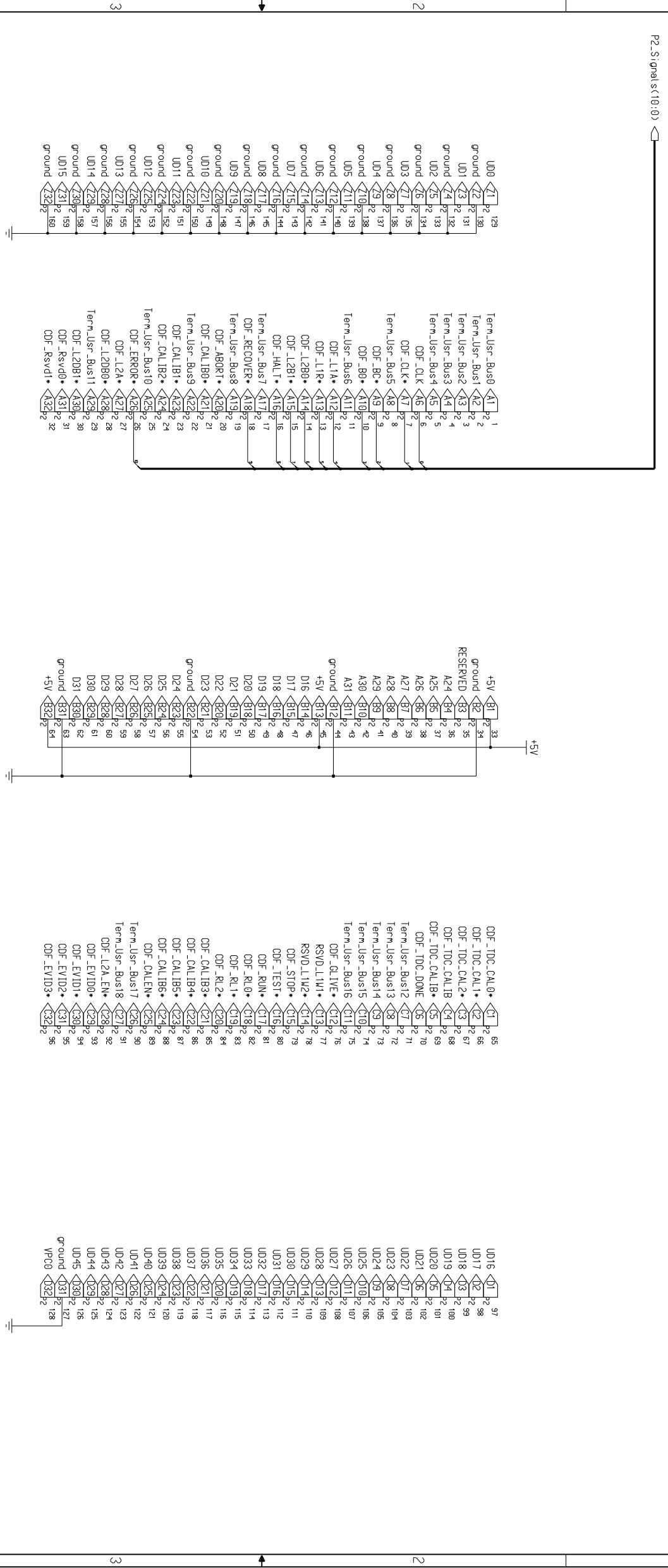


P2



Rows Z and D are in accordance with the VME64 Extensions standard. Note: the positioning of rows A, B and C should be in accordance with the VME Standard.

- All user-defined signals on P2 are totem-pole TTL excluding:
- a. Geographical Addresses are pulled-up/grounded wires
 - b. 132ns Clock is differential PECL: CLK - CLK*
 - c. ERROR* is open collector TTL

UNIVERSITY OF CHICAGO ELECTRONICS DEVELOPMENT GROUP		TITLE VMEbus_P2	
DATE	REVISION DESCRIPTION	SHEET	3 OF 10
16 Dec 93	Dr-0: final design -- YF	DATE	18 Nov 1996
31 Aug 95	Add row Z and B to conform to the VME64 Ext. Spec. Corrections to conform to new CDF P2 Specs.	DRN	JM
18 Nov 96	3rd Prototypes - Bus Files	REV	B