



UNIVERSITY OF CHICAGO
ELECTRONICS DEVELOPMENT GROUP

TITLE
PreFRED_logic

DATE	REVISION DESCRIPTION
15 May 1997	First Edition: after group review
17 Sep 1997	New design (parallel processing)
1 Oct 1997	Remove EPC1 after new configuration scheme

SHEET 10 OF 13	C-2392
DATE 17 Sep 1997	
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REV B	