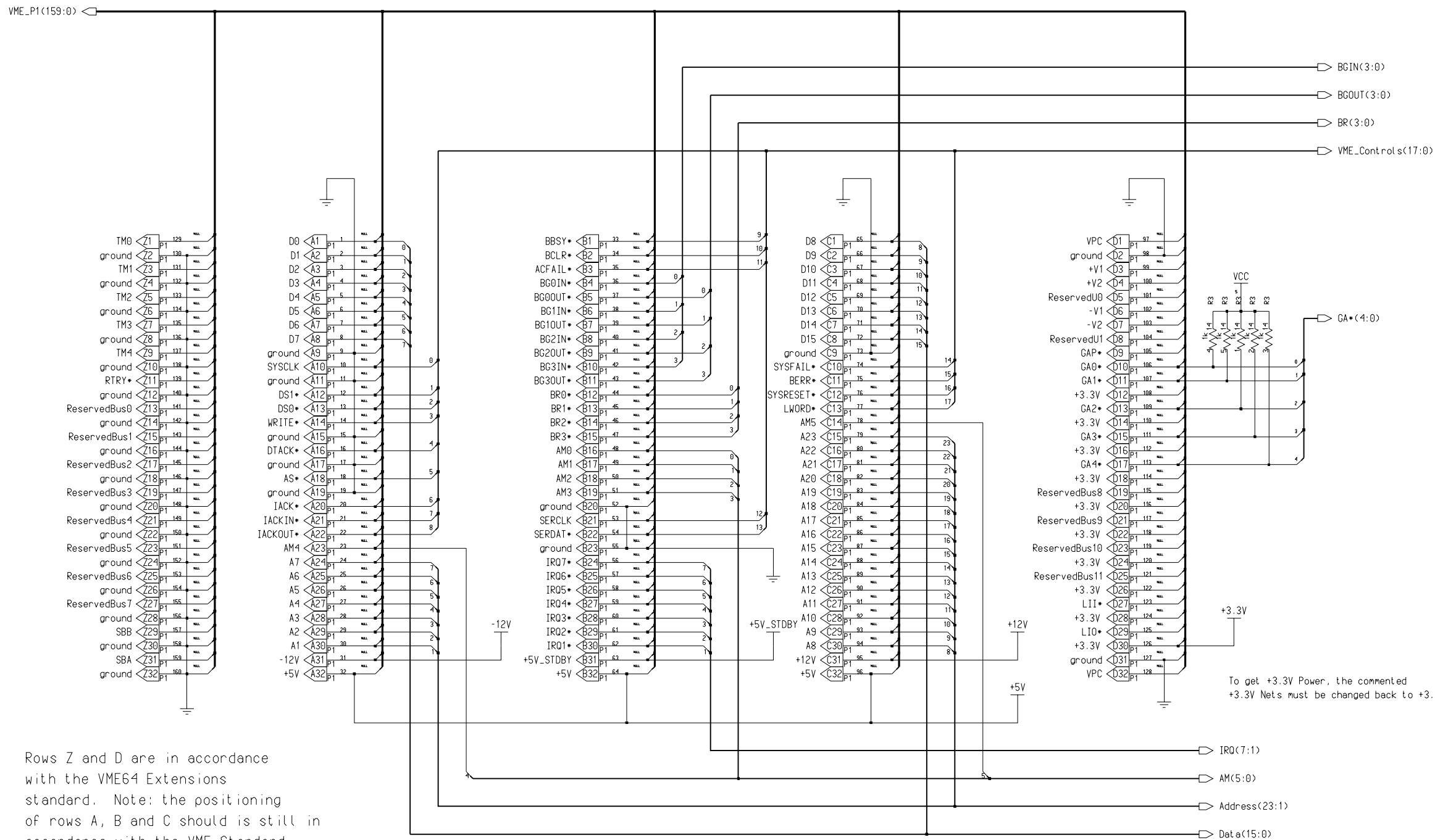


P1



Rows Z and D are in accordance with the VME64 Extensions standard. Note: the positioning of rows A, B and C should be in accordance with the VME Standard.

UNIVERSITY OF CHICAGO
ELECTRONICS DEVELOPMENT GROUP

TITLE

VMEbus_P1

DATE	REVISION DESCRIPTION
16 Dec 93	Original design -- YF
31 Aug 95	Add rows Z and D to conform to the VME64 Extensions Spec.
18 Nov 95	3rd Prototype - Bug Fixes

SHEET 3 OF 13
DATE 18 Nov 1996
DRWN JW

C-2392
REV B