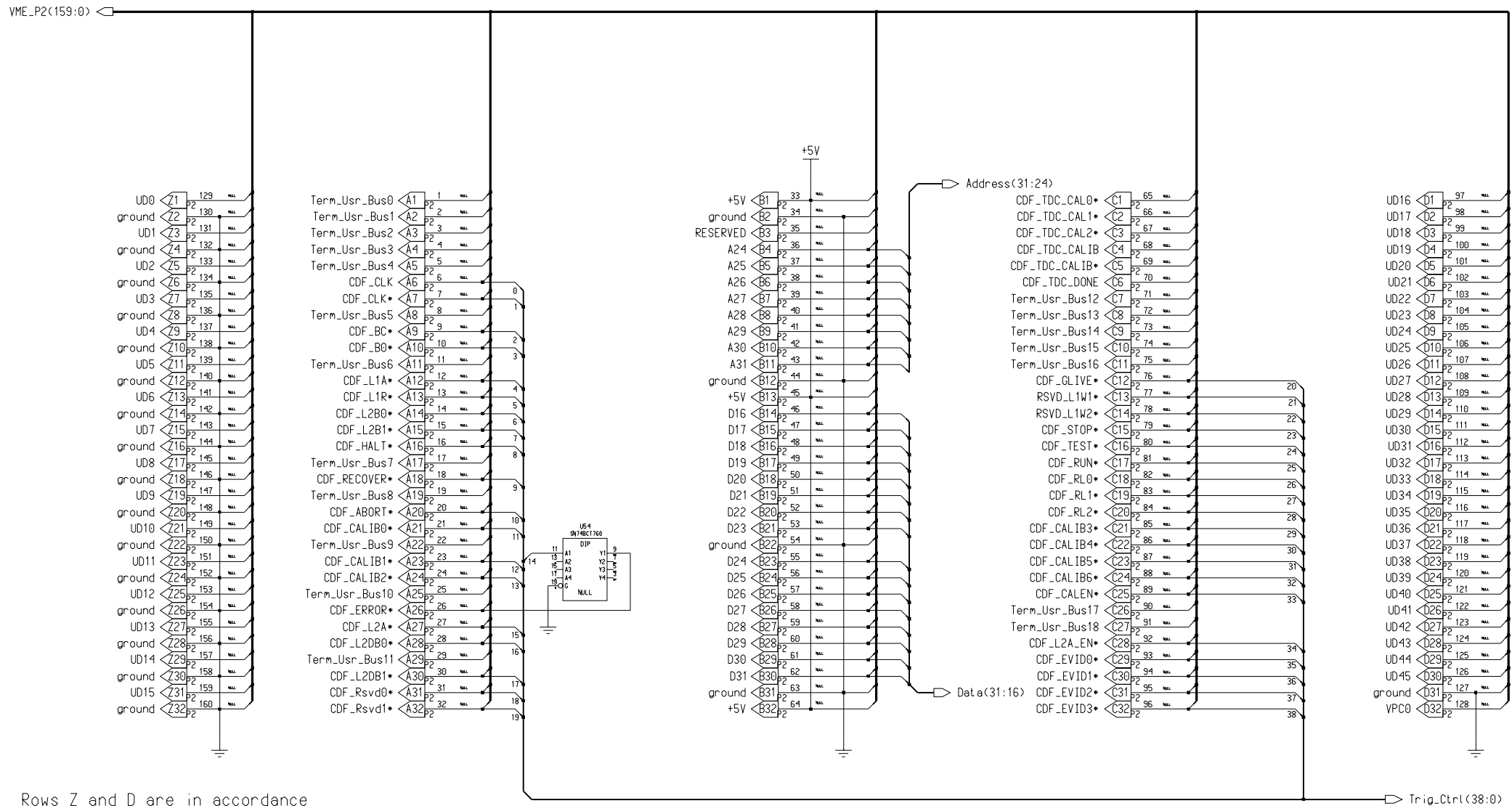


P2



Rows Z and D are in accordance with the VME64 Extensions standard. Note: the positioning of rows A, B and C should be still in accordance with the VME Standard.

All user-defined signals on P2 are totem-pole TTL excluding:

- Geographical Addresses are pulled-up/grounded wires
- 132ns Clock is differential PECL: CLK - CLK*
- ERROR* is open collector TTL

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TITLE VMEbus_P2	
DATE 16 Dec 93	REVISION DESCRIPTION Original design -- YF
DATE 31 Aug 95	REVISION DESCRIPTION Add rows Z and D to conform to the VME64 Ext. Spec. Corrections to conform to new GDF P2 Spec.
DATE 18 Nov 96	REVISION DESCRIPTION 3rd Prototype - Bug Fixes
SHEET DATE DRWN	5 OF 13 18 Nov 1996 JW
C-2392 REV B	

DATE	REVISION DESCRIPTION
16 Dec 93	Original design -- YF
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18 Nov 96	3rd Prototype - Bug Fixes