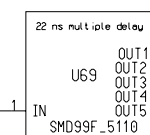
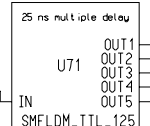
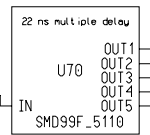
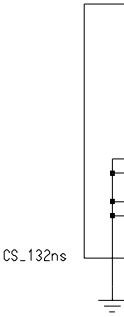
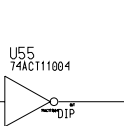
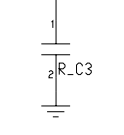
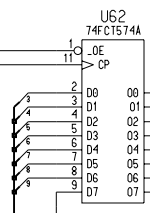
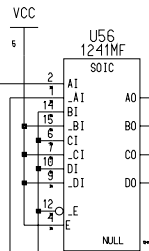


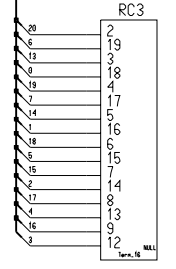
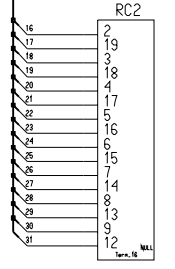
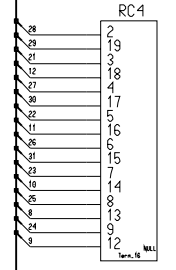
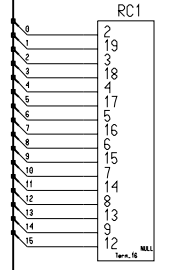
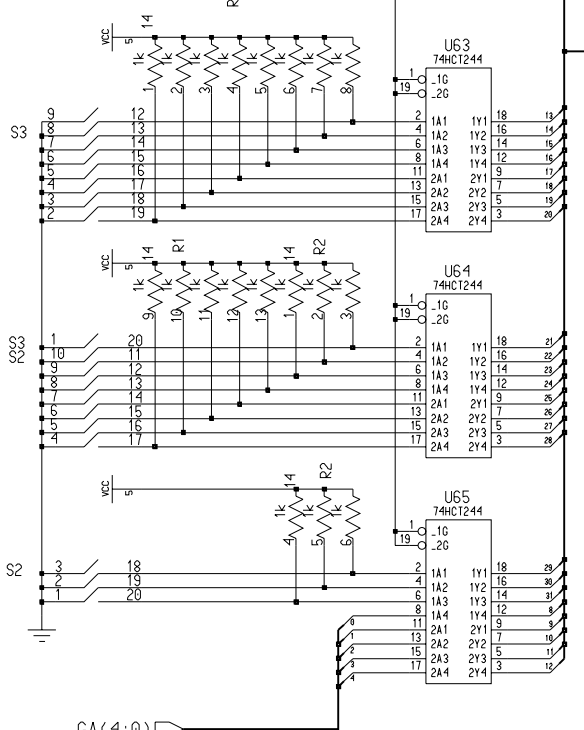
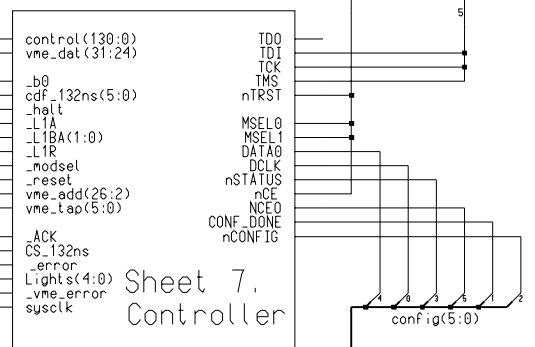
Address(26:2)

Trig\_Ctrl(38:0)

VME\_Control(7:0)



Clock generator



UNIVERSITY OF CHICAGO  
ELECTRONICS DEVELOPMENT GROUP

TITLE  
PreFRED / Control

DATE	REVISION DESCRIPTION
17 Jul 1997	Inspired from DIRAC Control
17 Sep 1997	Added the Controller
1 Oct 1997	Changed the configuration scheme

SHEET 6 OF 13  
DATE 17 Sep 1997  
DRWN Alexis / Mircea

C-2392  
REV B