



dout (22:0)

IF_dac_e.out (51:0)

*DSS
Mount on the FPGA side

hold_opp

DATE	REVISION DESCRIPTION

UNIVERSITY OF CHICAGO
ELECTRONICS DEVELOPMENT GROUP

Pulsar
Front_Panel/DATAOUT

SHEET	19 OF 25	C-2501
DATE	01 Feb 1999	REV A
DRN	T. Nakaya	