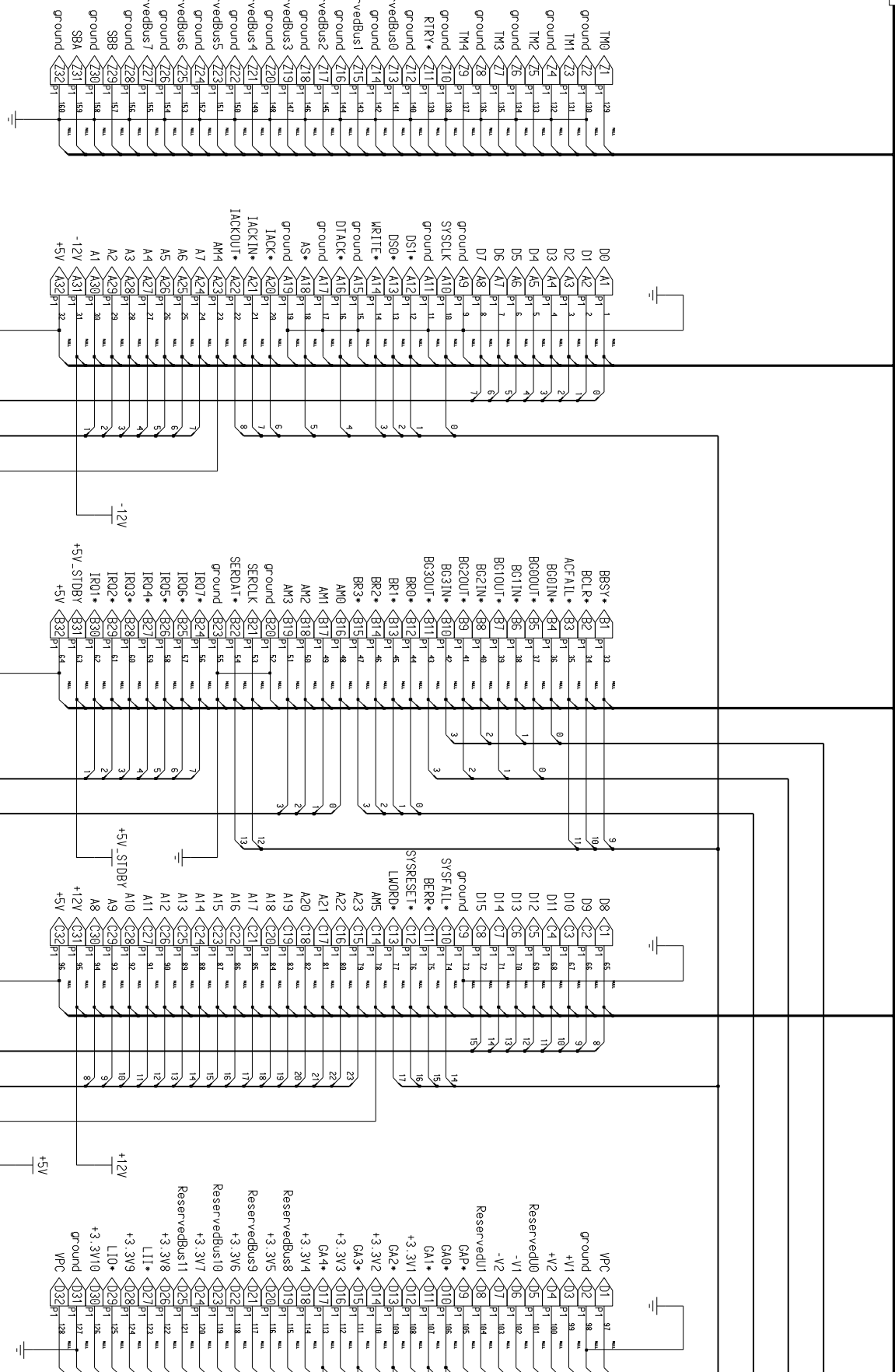


VME\_P1 (159:0)

P1



IRQ(7:1)  
 AM(5:0)  
 Address(23:1)  
 Data(15:0)

BGIN(3:0)  
 BCGOUT(3:0)  
 BR(3:0)  
 VME\_Control s(17:0)

VPC  
 Ground  
 +V1  
 +V2  
 +V4  
 +V5  
 +V6  
 +V7  
 +V8  
 +V9  
 +V10  
 +V11  
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 +V57  
 +V58  
 +V59  
 +V60  
 +V61  
 +V62  
 +V63

To get +3.3V Power, the commented  
 +3.3V Nets must be changed back to +3.3V

Rows Z and D are in accordance  
 with the VME64 Extensions  
 standard. Note: the positioning  
 of rows A, B and C should be in  
 accordance with the VME Standard.

UNIVERSITY OF CHICAGO ELECTRONICS DEVELOPMENT GROUP		
TITLE Pulsar VMEbus_P1		
DATE	REVISION DESCRIPTION	
1998-12-1	Copied from Pre-Redz P1 -- MBR/KJA	
2002-03-26	modified for Pulsar (MB/LJ)	
SHEET	8 OF 25	C-2501
DATE	18 Nov 1996	REV A
DRWN	JM	