

Fast Logic

Multiple Digital Delay Units

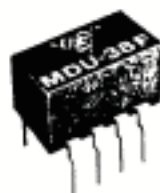
SERIES: MDU-38F
(8 pins DIP) TTL Interfaced

data delay devices, inc.



Features:

- Auto-insertable.
- Completely interfaced for TTL & DTL application.
- P.C. board space economy achieved.
- Fits standard 8 pins DIP socket.
- 3 equal delay units/package.

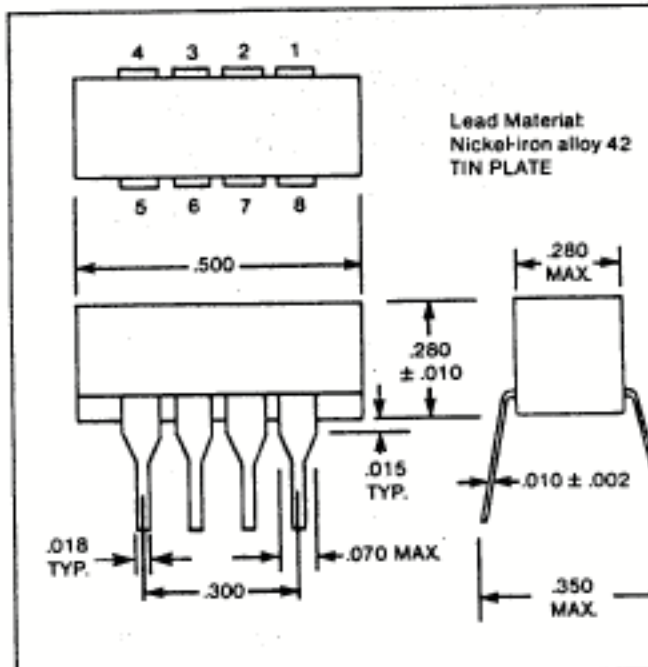


Specifications:

- **Delay Tolerance:** $\pm 5\%$ or 2 NS whichever is greater.
- **Rise-time:** 2 NS typically.
- **Temperature coefficient:** 100 PPM/ $^{\circ}$ C.
- **Temperature range:** 0° to $+70^{\circ}$ C.
- **Input signal requirement:** TTL logic.
- **Input pulse width:** Min. 100% of total delay.
- **Input rep. rate =** min. 3 X pulse width.
- **Output fan-out:** TTL schottky load.
- **Supply voltage:** 5 Vdc $\pm 5\%$.
- **Supply Current:**
I_{CC1}: 74 ma.
I_{CC2}: 14 ma.

Test Conditions:

- Input Pulse Width: $> 150\%$ of total delay.
- Time delay measured @ 1.5 V on rising edge.
- Rise-time is measured from .75 V to 2.4 V of leading time.
- All measurements made @ V_{CC} = 5V; T_A = $+25^{\circ}$ C, no loads on taps.



Part No.	Delay Per Line (NS)	Part No.	Delay Per Line (NS)
MDU-38F-5	5	MDU-38F-40	40
MDU-38F-10	10	MDU-38F-45	45
MDU-38F-15	15	MDU-38F-50	50
MDU-38F-20	20	MDU-38F-60	60
MDU-38F-25	25	MDU-38F-75	75
MDU-38F-30	30	MDU-38F-100	100
MDU-38F-35	35		

Other delay times available on request.

