



Engineer	M. Bogdan
Drawn by	M. Bogdan
R&D CHK	
DATE:	7/07/04
TIME:	2:00 pm
QA CHK	

The University of Chicago
 5640 S. Ellis Ave.
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TITLE	VME Chip <i>Time to Digital Converter</i>	Size
		C
REV B	DRW. C-2529	Sheet 4 of 16