TDC Firmware Status as of 9/15/04:
We have a firmware project compiled 9/10/04 with Quartus 4.1 that can work in VME32 mode on both our prototype boards and the new boards:
- It has a new VME64_data[31..0] bus included, but set in High Z;
- No LVDS_tx implementation.

Tested:
- Edge Detector works for up to 5 hits per channel;
- CBLT32 transfers tested.

Limitations:
- ED needs to work for up to 7 hits and Mary is working on that right now;
- XFT has some problems and Sasha will work on that next week.

We can use this firmware as is to test the new boards: ED and CBLT32.
XTC Firmware status

**XFT Testing Options:**

1. If we debug XFT Block Design Changes next week => O.K.

2. If not => implement simple counter inside chip to generate pulses on the XFT outputs => testing PCB connectivity to P3.

**CBLT64 Testing:**

More involved, more changes on the "main_chip" and "tdc_vme_chip" firmware designs.